

# CTVS – Ceramic transient voltage suppressors

SMD multilayer varistors (MLVs), standard series

Series/Type:

Date: December 2019

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#### Standard series

<u>SMD</u>

### EPCOS type designation system for standard series

СТ	0603	К	17	G
$\label{eq:construction:Construction:CT} \begin{split} & \text{Construction:} \\ & \text{CT} \triangleq \text{Single chip with nickel barrier termination} \\ & (\text{AgNiSn}) \end{split}$				
Case sizes: 0201 0402 0603 0805 1206 1210 1812 2220				
Tolerance of the varistor voltage: $K \triangleq \pm 10\%$ $L \triangleq \pm 15\%$ $M \triangleq \pm 20\%$ S, V $\triangleq$ Special tolerance				
Maximum RMS operating voltage (V <sub>RMS</sub> ): $17 \triangleq 17 \text{ V}$ Taping mode: $G \triangleq 180$ -mm reel, 7" $G2 \triangleq 330$ -mm reel, 13"				



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#### Features

- Reliable ESD protection up to 8 kV contact discharge and 15 kV air discharge acc. to IEC 61000-4-2, level 4
- Surge current up to 1200 A
- Bidirectional protection
- Long-term ESD stability
- RoHS-compatible, lead-free
- PSpice simulation models available

#### Applications

- ESD protection in mobile phones and accessories
- ESD protection in data bus applications
- ESD protection in control electronics, lighting/ LED and medical devices
- Surge current protection in smart meters and measurement equipment
- Surge current protection in security and safety systems

#### Design

- Multilayer technology
- Flammability rating better than UL 94 V-0

### V/I characteristics and derating curves

V/l and derating curves are attached to the data sheet. The curves are sorted by  $V_{\text{RMS}}$  and then by case size, which is included in the type designation.

### Single chip

Internal circuit



MLV0006-H

Available case sizes:

EIA	Metric
0201	0603
0402	1005
0603	1608
0805	2012
1206	3216
1210	3225
1812	4532
2220	5750



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### General technical data

Maximum RMS operating voltage		$V_{\text{RMS,max}}$	4 130	V
Maximum DC operating voltage		$V_{\text{DC,max}}$	5.5 170	V
Maximum surge current	(8/20 µs)	I <sub>surge,max</sub>	2 1200	А
Maximum energy absorption	(2 ms)	W <sub>max</sub>	7.5 12000	mJ
Maximum power dissipation		$P_{diss,max}$	3 20	mW
Maximum clamping voltage		$V_{\text{clamp,max}}$	17 340	V
Operating temperature	for case size 0201, 0402	T <sub>op</sub>	-40/+85	°C
	for case size $\geq$ 0603	T <sub>op</sub>	-55/+125	°C
Storage temperature	for case size 0201, 0402	LCT/UCT	-40/+125	°C
	for case size $\geq$ 0603	LCT/UCT	-55/+150	°C
Response time		t <sub>resp</sub>	< 0.5	ns



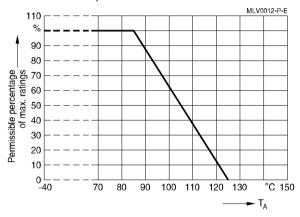
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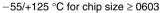
#### **Temperature derating**

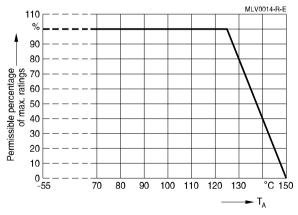
Climatic category:

-40/+85 °C for chip sizes 0201 and 0402



Climatic category:





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### Electrical specifications and ordering codes

Maximum ratings (T<sub>op,max</sub>)

Туре	Ordering code	$V_{\text{RMS,max}}$	$V_{\text{DC,max}}$	I <sub>surge,max</sub> (8/20 μs)	W <sub>max</sub> (2 ms)	$P_{diss,max}$	T <sub>op,max</sub>
		v	v	A	mJ	mW	°C
CT standard series	I	1	<u>.</u>	<u> </u>		<u> </u>	<u> </u>
CT0201S4AHSG	B72440T8040S160	4	5.5	-	-	-	+85
CT0402L4G	B72590T0040L060	4	5.5	20	30	3	+85
CT0402M4G	B72590T0040M060	4	5.5	20	7.5	3	+85
CT0402S5ARFG	B72590T7050S160	4	5.5	-	-	-	+85
CT0603M4G	B72500T0040M060	4	5.5	30	100	3	+125
CT0603S5ARFG	B72500T7050S160	4	5.5	-	-	-	+125
CT0805M4G	B72510T0040M062	4	5.5	100	100	5	+125
CT1206M4G	B72520T0040M062	4	5.5	150	300	8	+125
CT1210M4G	B72530T0040M062	4	5.5	250	400	10	+125
CT1812M4G	B72580T0040M062	4	5.5	500	800	15	+125
CT2220M4G	B72540T0040M062	4	5.5	1000	1400	20	+125
CT0603M6G	B72500T0060M060	6	8	30	100	3	+125
CT0805M6G	B72510T0060M062	6	8	120	200	5	+125
CT1206M6G	B72520T0060M062	6	8	200	400	8	+125
CT1210M6G	B72530T0060M062	6	8	300	700	10	+125
CT1812M6G	B72580T0060M062	6	8	500	1000	15	+125
CT2220M6G	B72540T0060M062	6	8	1200	3600	20	+125
CT0603K7G	B72500T0070K060	7	9	30	100	3	+125
CT0603M7G	B72500T0070M060	7	9	30	100	3	+125
CT0603L8G	B72500T0080L060	8	11	30	100	3	+125
CT0805L8G	B72510T0080L062	8	11	120	200	5	+125
CT1206L8G	B72520T0080L062	8	11	200	500	8	+125
CT1210L8G	B72530T0080L062	8	11	400	1000	10	+125
CT1812L8G	B72580T0080L062	8	11	800	1800	15	+125
CT2220L8G	B72540T0080L062	8	11	1200	4200	20	+125
CT0402S11ACCG	B72590T0110S460	11	12	22	7.5	3	+85
CT0402S11AG	B72590T0110S160	11	14	22	7.5	3	+85
CT0402S11AGK2	B72590T0110S360	11	14	22	7.5	3	+85
CT0603K11G	B72500T0110K060	11	14	30	200	3	+125
CT0805K11G	B72510T0110K062	11	14	120	200	5	+125
CT1206K11G	B72520T0110K062	11	14	200	500	8	+125
CT1210K11G	B72530T0110K062	11	14	400	1200	10	+125
CT1812K11G	B72580T0110K062	11	14	800	1900	15	+125
CT2220K11G	B72540T0110K062	11	14	1200	5400	20	+125



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# Characteristics (T<sub>A</sub> = 25 $^{\circ}$ C)

Туре	Vv	$\Delta V_V$	$V_{\text{clamp,max}}$	I <sub>clamp</sub>	C <sub>typ</sub> <sup>1)</sup>
51	(1 mA)			(8/20 µs)	(@1V)
	V	%	V	A	pF
CT standard series					
CT0201S4AHSG	30	±30	70	1	10
CT0402L4G	23.5	±15	46	1	47
CT0402M4G	10	±20	24	1	200
CT0402S5ARFG	255	±15	-	-	0.6
CT0603M4G	8	±20	19	1	200
CT0603S5ARFG	255	±15	-	-	0.6
CT0805M4G	8	±20	19	1	700
CT1206M4G	8	±20	17	1	1500
CT1210M4G	8	±20	17	2.5	5000
CT1812M4G	8	±20	17	5	10000
CT2220M4G	8	±20	17	10	24000
CT0603M6G	11	±20	27	1	200
CT0805M6G	11	±20	27	1	600
CT1206M6G	11	±20	25	1	1200
CT1210M6G	11	±20	25	2.5	4000
CT1812M6G	11	±20	25	5	8000
CT2220M6G	11	±20	25	10	24000
CT0603K7G	12.5	±10	27	1	130
CT0603M7G	12.5	±20	30	1	200
CT0603L8G	15	±15	33	1	150
CT0805L8G	15	±15	33	1	500
CT1206L8G	15	±15	30	1	1000
CT1210L8G	15	±15	30	2.5	3000
CT1812L8G	15	±15	30	5	6000
CT2220L8G	15	±15	30	10	16000
CT0402S11ACCG	18	±25	40	1	120 <sup>2)</sup>
CT0402S11AG	18.5	±15	35	1	120
CT0402S11AGK2	18.5	±15	35	1	100
CT0603K11G	18	±10	35	1	100
CT0805K11G	18	±10	35	1	400
CT1206K11G	18	±10	33	1	800
CT1210K11G	18	±10	33	2.5	2400
CT1812K11G	18	±10	33	5	5000
CT2220K11G	18	±10	33	10	12000

1) Measurement frequency: f = 1 MHz for C < 100 pF, f = 1 kHz for C  $\geq$  100 pF

2) Controlled capacitance: C<sub>min</sub> = 96 pF, C<sub>max</sub> = 144 pF

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### Electrical specifications and ordering codes

Maximum ratings (T<sub>op,max</sub>)

Туре	Ordering code	$V_{\text{RMS,max}}$	$V_{\text{DC},\text{max}}$	I <sub>surge,max</sub>	W <sub>max</sub>	$P_{diss,max}$	$T_{op,max}$
				(8/20 µs)	(2 ms)		°C
CT standard series		V	V	A	mJ	mW	Ű
			10		10		0.5
CT0402L14G	B72590T0140L060	14	16	20	10	3	+85
CT0402L14UG	B72590T0140L960	14	16	10	10	3	+85
CT0402S14AHSG	B72590T8140S160	14	16	2	-	3	+85
CT0402V150HSG	B72590T8151V060	14	16	-	-	-	+85
CT0402V150RFG	B72590T7151V060	14	16	-	-	-	+85
CT0402V275RFG	B72590T7271V060	14	16	-	-	-	+85
CT0402V90RFG	B72590T7900V060	14	16	-	-	-	+85
CT0603K14G	B72500T0140K060	14	18	30	200	3	+125
CT0603S14AHSG	B72500T8140S160	14	16	5	-	3	+125
CT0603V150RFG	B72500T7151V060	14	16	-	-	-	+125
CT0805K14G	B72510T0140K062	14	18	120	300	5	+125
CT1206K14G	B72520T0140K062	14	18	200	500	8	+125
CT1210K14G	B72530T0140K062	14	18	400	1500	10	+125
CT0402S17AG	B72590T0170S160	17	19	20	10	3	+85
CT1206K17G	B72520T0170K062	17	22	200	600	8	+125
CT1206K20G	B72520T0200K062	20	26	200	700	8	+125
CT0603K25G	B72500T0250K060	25	31	30	300	3	+125
CT0603L25HSG	B72500T8250L060	25	32	5	-	-	+125
CT0805K25G	B72510T0250K062	25	31	80	300	5	+125
CT1206K25G	B72520T0250K062	25	31	200	1000	8	+125
CT0805K30G	B72510T0300K062	30	38	80	300	5	+125
CT1206K30G	B72520T0300K062	30	38	200	1100	8	+125
CT1210K30G	B72530T0300K062	30	38	300	2000	10	+125
CT1812K30G	B72580T0300K062	30	38	800	4200	15	+125
CT2220K30G	B72540T0300K062	30	38	1200	12000	20	+125
CT0805K35G	B72510T0350K062	35	45	80	300	5	+125
CT1206K35G	B72520T0350K062	35	45	100	400	8	+125
CT1210K35G	B72530T0350K062	35	45	250	2000	10	+125
CT1812K35G	B72580T0350K062	35	45	500	4000	15	+125
CT1206K40G	B72520T0400K062	40	56	100	500	8	+125
CT1210K40G	B72530T0400K062	40	56	250	2300	10	+125
CT1812K40G	B72580T0400K062	40	56	500	4800	15	+125
CT2220K40G	B72540T0400K062	40	56	1000	9000	20	+125
CT1206K50G	B72520T0500K062	50	65	100	600	8	+125
CT1210K50G	B72530T0500K062	50	65	200	1600	10	+125
CT1812K50G	B72580T0500K062	50	65	400	4500	15	+125



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# Characteristics (T<sub>A</sub> = 25 $^{\circ}$ C)

Turne	Vv	$\Delta V_{V}$	V	Ti	C <sub>typ</sub> <sup>1)</sup>
Туре	v <sub>v</sub> (1 mA)	$\Delta \mathbf{v}_{V}$	V <sub>clamp,max</sub>	I <sub>clamp</sub> (8/20 μs)	(@ 1 V)
	(TIIIA) V	%	v	· · · /	pF
CT standard series	V	70	V	A	рг
		T		I .	I
CT0402L14G	23.5	±15	46	1	47
CT0402L14UG	23.5	±15	46	1	47
CT0402S14AHSG	28	±20	66	1	10
CT0402V150HSG	175	±15	290	1	-
CT0402V150RFG	175	±15	290	1	2
CT0402V275RFG	275	±30	-	-	1.5
CT0402V90RFG	105	±15	-	-	2.2
CT0603K14G	22	±10	40	1	100
CT0603S14AHSG	28	±20	66	1	-
CT0603V150RFG	175	±15	290	1	3
CT0805K14G	22	±10	40	1	350
CT1206K14G	22	±10	38	1	700
CT1210K14G	22	±10	38	2.5	2000
CT0402S17AG	32.5	±25	59	1	33
CT1206K17G	27	±10	44	1	650
CT1206K20G	33	±10	54	1	600
CT0603K25G	39	±10	67	1	90
CT0603L25HSG	61	±15	120	1	-
CT0805K25G	39	±10	67	1	250
CT1206K25G	39	±10	65	1	550
CT0805K30G	47	±10	77	1	200
CT1206K30G	47	±10	77	1	500
CT1210K30G	47	±10	77	2.5	1000
CT1812K30G	47	±10	77	5	2000
CT2220K30G	47	±10	77	10	4000
CT0805K35G	56	±10	95	1	150
CT1206K35G	56	±10	90	1	200
CT1210K35G	56	±10	90	2.5	600
CT1812K35G	56	±10	90	5	1200
CT1206K40G	68	±10	110	1	250
CT1210K40G	68	±10	110	2.5	500
CT1812K40G	68	±10	110	5	1000
CT2220K40G	68	±10	110	10	2000
CT1206K50G	82	±10	135	1	120
CT1210K50G	82	±10	135	2.5	250
CT1812K50G	82	±10	135	5	500

1) Measurement frequency: f = 1 MHz for C < 100 pF, f = 1 kHz for C  $\ge$  100 pF



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# Electrical specifications and ordering codes

Maximum ratings (T<sub>op,max</sub>)

Туре	Ordering code	V <sub>RMS,max</sub>	$V_{\text{DC,max}}$	I <sub>surge,max</sub> (8/20 μs)	W <sub>max</sub> (2 ms)	$P_{diss,max}$	T <sub>op,max</sub>
		V	V	A	mJ	mW	°C
CT standard series							
CT2220K50G	B72540T0500K062	50	65	800	5600	20	+125
CT1206K60G	B72520T0600K062	60	85	100	700	8	+125
CT1210K60G	B72530T0600K062	60	85	200	2000	10	+125
CT1812K60G	B72580T0600K062	60	85	400	5800	15	+125
CT2220K60G	B72540T0600K062	60	85	800	6800	20	+125
CT1812K130G2	B72580T0131K072	130	170	250	3500	15	+125

# Characteristics (T<sub>A</sub> = 25 $^{\circ}$ C)

Туре	Vv	$\Delta V_V$	V <sub>clamp,max</sub>	I <sub>clamp</sub>	C <sub>typ</sub> <sup>1)</sup>
	(1 mA)			(8/20 µs)	(@1V)
	V	%	V	A	pF
CT standard series					
CT2220K50G	82	±10	135	10	1000
CT1206K60G	100	±10	165	1	100
CT1210K60G	100	±10	165	2.5	200
CT1812K60G	100	±10	165	5	400
CT2220K60G	100	±10	165	10	800
CT1812K130G2	205	±10	340	5	200

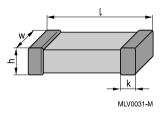
1) Measurement frequency: f = 1 MHz for C < 100 pF, f = 1 kHz for C  $\ge$  100 pF



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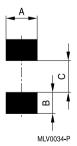
#### **Dimensional drawing**



#### Dimensions in mm

Case size EIA / mm	I	w	h	k
0201 / 0603	0.6 ±0.03	0.30 ±0.03	0.33 max.	0.15 ±0.05
0402 / 1005	1.0 ±0.15	0.50 ±0.10	0.6 max.	0.10 0.30
0603 / 1608	1.6 ±0.15	0.80 ±0.10	0.9 max.	0.10 0.40
0805 / 2012	2.0 ±0.20	1.25 ±0.15	1.4 max.	0.13 0.75
1206 / 3216	3.2 ±0.30	1.60 ±0.20	1.7 max.	0.25 0.75
1210 / 3225	3.2 ±0.30	2.50 ±0.25	1.7 max.	0.25 0.75
1812 / 4532	4.5 ±0.40	3.20 ±0.30	2.5 max.	0.25 1.00
2220 / 5750	5.7 ±0.40	5.00 ±0.40	2.5 max.	0.25 1.00

# Recommended solder pad layout



#### Dimensions in mm

Case size	А	В	С
EIA / mm			
0201 / 0603	0.30	0.25	0.30
0402 / 1005	0.60	0.60	0.50
0603 / 1608	1.00	1.00	1.00
0805 / 2012	1.40	1.20	1.00
1206 / 3216	1.80	1.20	2.10
1210 / 3225	2.80	1.20	2.10
1812 / 4532	3.60	1.50	3.00
2220 / 5750	5.50	1.50	4.20



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# Delivery mode

EIA case size	Taping	Reel size	Packing unit	Туре	Ordering code
		mm	pcs.		-
0201	Cardboard	180	15000	CT0201S4AHSG	B72440T8040S160
0402	Cardboard	180	10000	CT0402L14G	B72590T0140L060
0402	Cardboard	180	10000	CT0402L14UG	B72590T0140L960
0402	Cardboard	180	10000	CT0402L4G	B72590T0040L060
0402	Cardboard	180	10000	CT0402M4G	B72590T0040M060
0402	Cardboard	180	10000	CT0402S11ACCG	B72590T0110S460
0402	Cardboard	180	10000	CT0402S11AG	B72590T0110S160
0402	Cardboard	180	10000	CT0402S11AGK2	B72590T0110S360
0402	Cardboard	180	10000	CT0402S14AHSG	B72590T8140S160
0402	Cardboard	180	10000	CT0402S17AG	B72590T0170S160
0402	Cardboard	180	10000	CT0402S5ARFG	B72590T7050S160
0402	Cardboard	180	10000	CT0402V150HSG	B72590T8151V060
0402	Cardboard	180	10000	CT0402V150RFG	B72590T7151V060
0402	Cardboard	180	10000	CT0402V275RFG	B72590T7271V060
0402	Cardboard	180	10000	CT0402V90RFG	B72590T7900V060
0603	Cardboard	180	4000	CT0603K11G	B72500T0110K060
0603	Cardboard	180	4000	CT0603K14G	B72500T0140K060
0603	Cardboard	180	4000	CT0603K25G	B72500T0250K060
0603	Cardboard	180	4000	CT0603K7G	B72500T0070K060
0603	Cardboard	180	4000	CT0603L25HSG	B72500T8250L060
0603	Cardboard	180	4000	CT0603L8G	B72500T0080L060
0603	Cardboard	180	4000	CT0603M4G	B72500T0040M060
0603	Cardboard	180	4000	CT0603M6G	B72500T0060M060
0603	Cardboard	180	4000	CT0603M7G	B72500T0070M060
0603	Cardboard	180	4000	CT0603S14AHSG	B72500T8140S160
0603	Cardboard	180	4000	CT0603S5ARFG	B72500T7050S160
0603	Cardboard	180	4000	CT0603V150RFG	B72500T7151V060
0805	Blister	180	3000	CT0805K11G	B72510T0110K062
0805	Blister	180	3000	CT0805K14G	B72510T0140K062
0805	Blister	180	3000	CT0805K25G	B72510T0250K062
0805	Blister	180	3000	CT0805K30G	B72510T0300K062
0805	Blister	180	3000	CT0805K35G	B72510T0350K062
0805	Blister	180	3000	CT0805L8G	B72510T0080L062
0805	Blister	180	3000	CT0805M4G	B72510T0040M062
0805	Blister	180	3000	CT0805M6G	B72510T0060M062
1206	Blister	180	3000	CT1206K11G	B72520T0110K062
1206	Blister	180	3000	CT1206K14G	B72520T0140K062
1206	Blister	180	3000	CT1206K17G	B72520T0170K062
1206	Blister	180	3000	CT1206K20G	B72520T0200K062
1206	Blister	180	2000	CT1206K25G	B72520T0250K062
1206	Blister	180	2000	CT1206K30G	B72520T0300K062
1206	Blister	180	2000	CT1206K35G	B72520T0350K062



Standard series

SMD					
EIA case size	Taping	Reel size	Packing unit	Туре	Ordering code
		mm	pcs.		-
1206	Blister	180	2000	CT1206K40G	B72520T0400K062
1206	Blister	180	2000	CT1206K50G	B72520T0500K062
1206	Blister	180	2000	CT1206K60G	B72520T0600K062
1206	Blister	180	3000	CT1206L8G	B72520T0080L062
1206	Blister	180	3000	CT1206M4G	B72520T0040M062
1206	Blister	180	3000	CT1206M6G	B72520T0060M062
1210	Blister	180	3000	CT1210K11G	B72530T0110K062
1210	Blister	180	3000	CT1210K14G	B72530T0140K062
1210	Blister	180	2000	CT1210K30G	B72530T0300K062
1210	Blister	180	2000	CT1210K35G	B72530T0350K062
1210	Blister	180	2000	CT1210K40G	B72530T0400K062
1210	Blister	180	2000	CT1210K50G	B72530T0500K062
1210	Blister	180	2000	CT1210K60G	B72530T0600K062
1210	Blister	180	3000	CT1210L8G	B72530T0080L062
1210	Blister	180	3000	CT1210M4G	B72530T0040M062
1210	Blister	180	3000	CT1210M6G	B72530T0060M062
1812	Blister	180	1500	CT1812K11G	B72580T0110K062
1812	Blister	330	3000	CT1812K130G2	B72580T0131K072
1812	Blister	180	1000	CT1812K30G	B72580T0300K062
1812	Blister	180	1000	CT1812K35G	B72580T0350K062
1812	Blister	180	1000	CT1812K40G	B72580T0400K062
1812	Blister	180	1000	CT1812K50G	B72580T0500K062
1812	Blister	180	1000	CT1812K60G	B72580T0600K062
1812	Blister	180	1500	CT1812L8G	B72580T0080L062
1812	Blister	180	1500	CT1812M4G	B72580T0040M062
1812	Blister	180	1500	CT1812M6G	B72580T0060M062
2220	Blister	180	1500	CT2220K11G	B72540T0110K062
2220	Blister	180	1000	CT2220K30G	B72540T0300K062
2220	Blister	180	1000	CT2220K40G	B72540T0400K062
2220	Blister	180	1000	CT2220K50G	B72540T0500K062
2220	Blister	180	1000	CT2220K60G	B72540T0600K062
2220	Blister	180	1500	CT2220L8G	B72540T0080L062
2220	Blister	180	1500	CT2220M4G	B72540T0040M062
2220	Blister	180	1500	CT2220M6G	B72540T0060M062

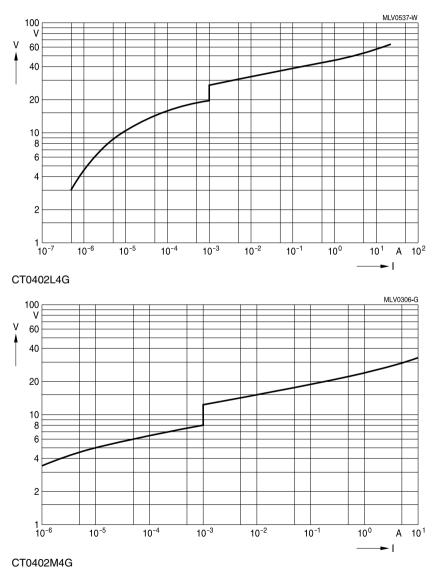


#### Standard series

# <u>SMD</u>

### V/I characteristics for standard series

Not specified for following types: CT0201S4AHSG, CT0402S5ARFG, CT0402V150HSG, CT0402V150RFG, CT0402V275RFG, CT0402V90RFG, CT0603S5ARFG and CT0603V150RFG.



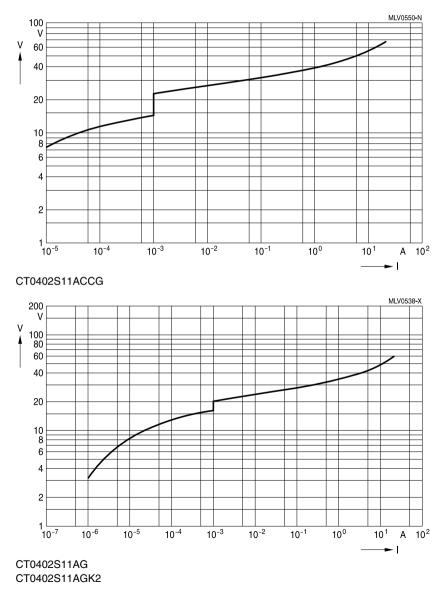


#### Standard series

# <u>SMD</u>

### V/I characteristics for standard series

Not specified for following types: CT0201S4AHSG, CT0402S5ARFG, CT0402V150HSG, CT0402V150RFG, CT0402V255RFG, CT0402V90RFG, CT0603S5ARFG and CT0603V150RFG.



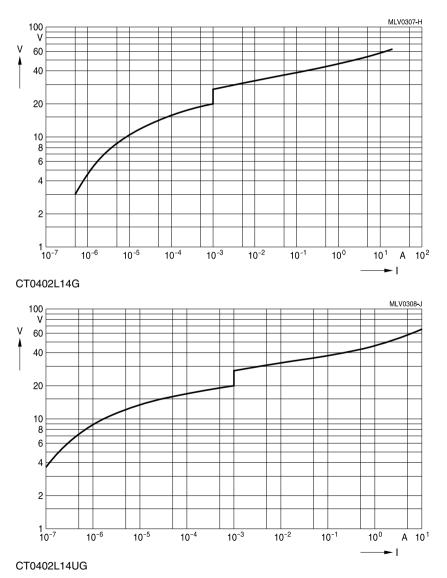


#### Standard series

# <u>SMD</u>

#### V/I characteristics for standard series

Not specified for following types: CT0201S4AHSG, CT0402S5ARFG, CT0402V150HSG, CT0402V150RFG, CT0402V255RFG, CT0402V90RFG, CT0603S5ARFG and CT0603V150RFG.



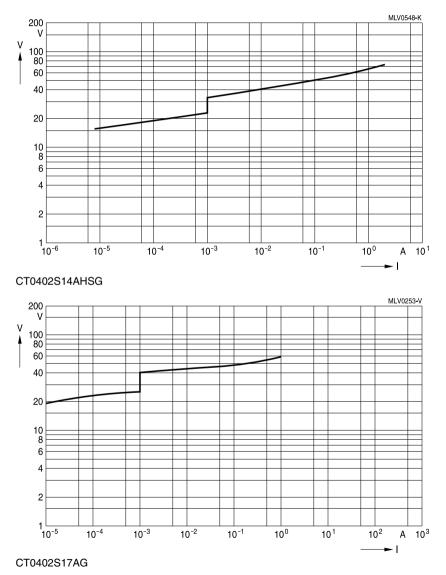


#### Standard series

# <u>SMD</u>

### V/I characteristics for standard series

Not specified for following types: CT0201S4AHSG, CT0402S5ARFG, CT0402V150HSG, CT0402V150RFG, CT0402V255RFG, CT0402V90RFG, CT0603S5ARFG and CT0603V150RFG.



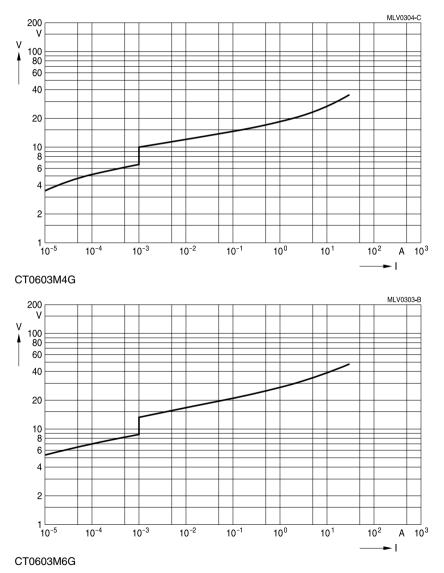


#### Standard series

# <u>SMD</u>

### V/I characteristics for standard series

Not specified for following types: CT0201S4AHSG, CT0402S5ARFG, CT0402V150HSG, CT0402V150RFG, CT0402V255RFG, CT0402V90RFG, CT0603S5ARFG and CT0603V150RFG.



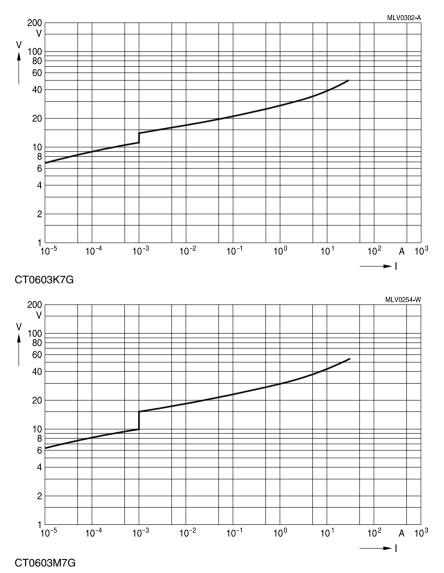


#### Standard series

# <u>SMD</u>

#### V/I characteristics for standard series

Not specified for following types: CT0201S4AHSG, CT0402S5ARFG, CT0402V150HSG, CT0402V150RFG, CT0402V255RFG, CT0402V90RFG, CT0603S5ARFG and CT0603V150RFG.



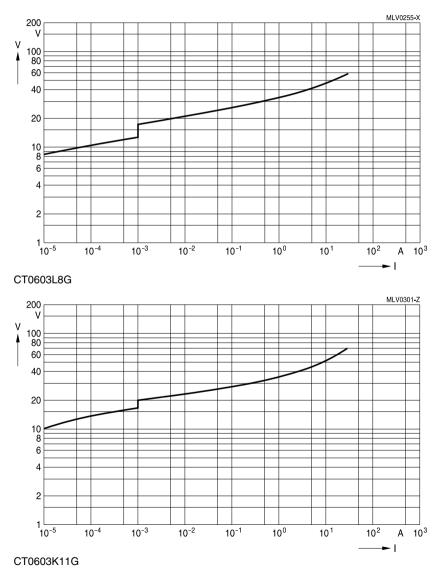


#### Standard series

# <u>SMD</u>

### V/I characteristics for standard series

Not specified for following types: CT0201S4AHSG, CT0402S5ARFG, CT0402V150HSG, CT0402V150RFG, CT0402V255RFG, CT0402V90RFG, CT0603S5ARFG and CT0603V150RFG.



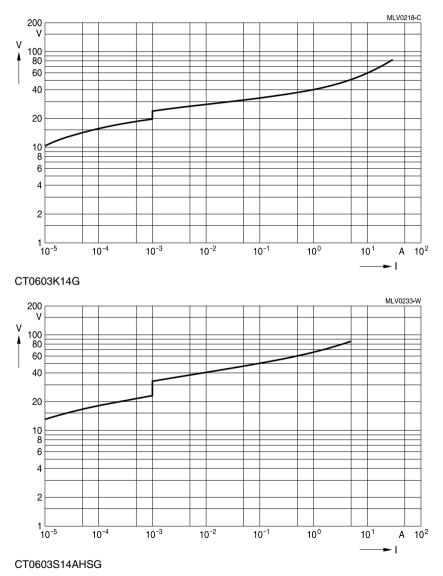


#### Standard series

# <u>SMD</u>

### V/I characteristics for standard series

Not specified for following types: CT0201S4AHSG, CT0402S5ARFG, CT0402V150HSG, CT0402V150RFG, CT0402V275RFG, CT0402V90RFG, CT0603S5ARFG and CT0603V150RFG.



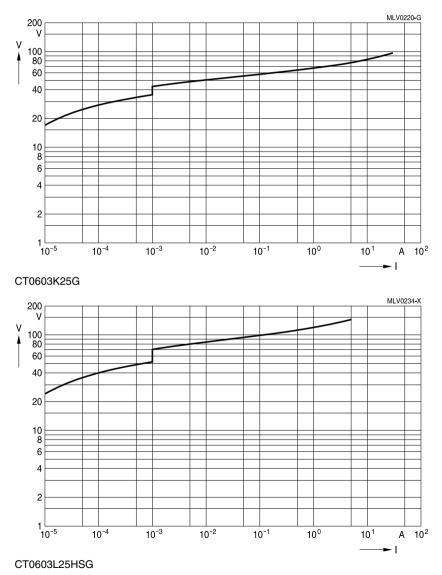


#### Standard series

# <u>SMD</u>

### V/I characteristics for standard series

Not specified for following types: CT0201S4AHSG, CT0402S5ARFG, CT0402V150HSG, CT0402V150RFG, CT0402V255RFG, CT0402V90RFG, CT0603S5ARFG and CT0603V150RFG.

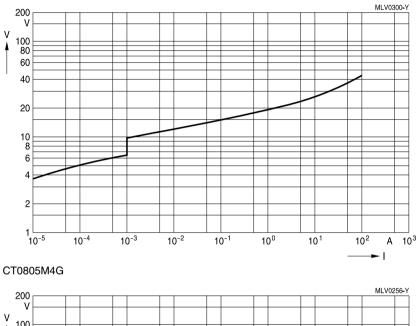


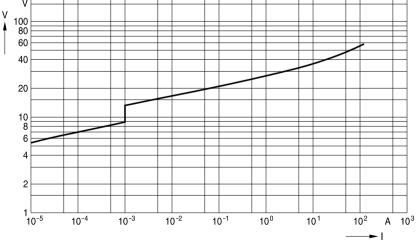


#### Standard series

<u>SMD</u>

### V/I characteristics for standard series





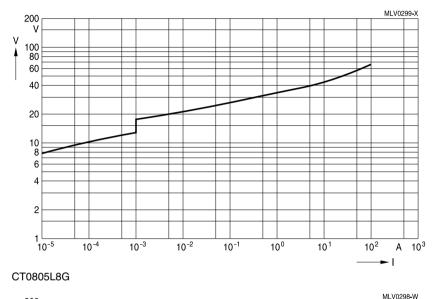
CT0805M6G

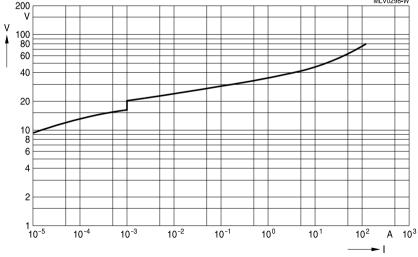


#### Standard series

<u>SMD</u>

### V/I characteristics for standard series





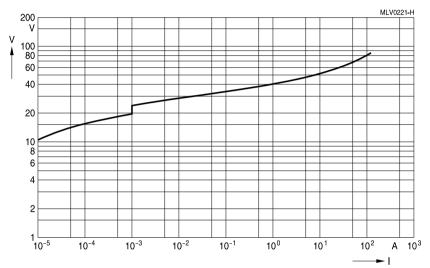
CT0805K11G



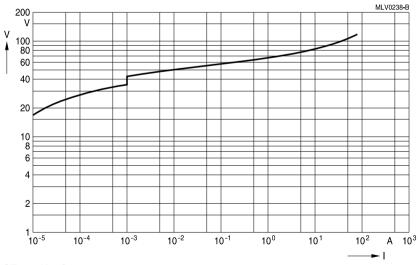
Standard series

<u>SMD</u>

### V/I characteristics for standard series



CT0805K14G



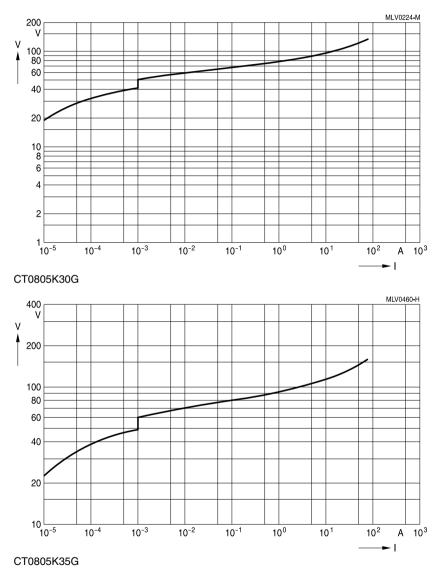
CT0805K25G



#### Standard series

<u>SMD</u>

### V/I characteristics for standard series

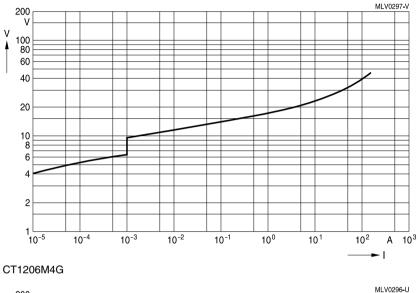


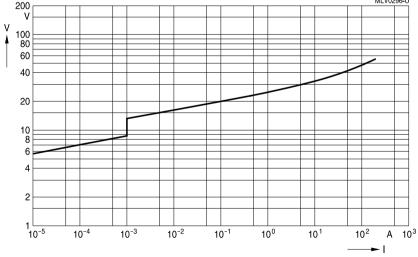


#### Standard series

<u>SMD</u>

### V/I characteristics for standard series





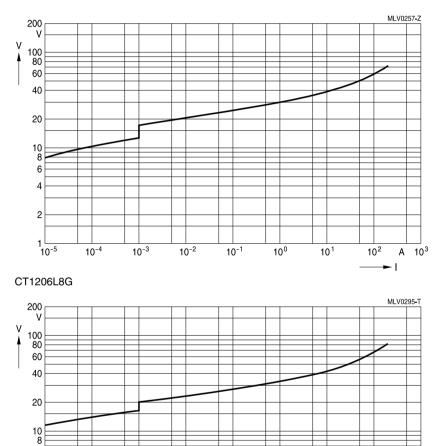
CT1206M6G



Standard series

<u>SMD</u>

### V/I characteristics for standard series



CT1206K11G

1 └── 10<sup>-5</sup>

10<sup>-4</sup>

10<sup>-3</sup>

6 4

2

10<sup>-1</sup>

10<sup>0</sup>

10<sup>1</sup>

10<sup>2</sup>

A 10<sup>3</sup>

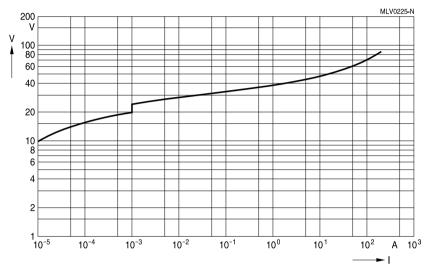
10<sup>-2</sup>



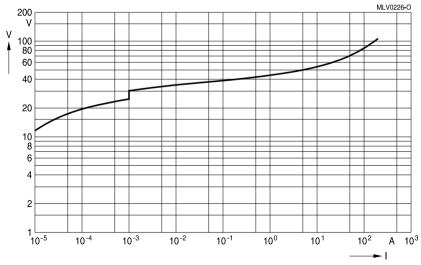
#### Standard series

<u>SMD</u>

### V/I characteristics for standard series



CT1206K14G



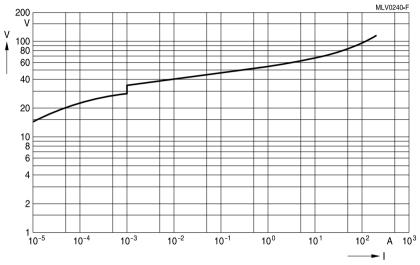
CT1206K17G



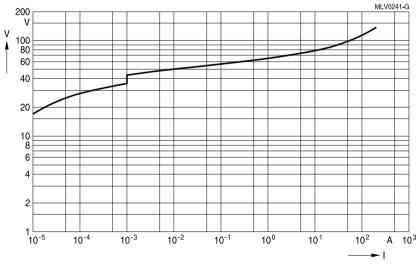
#### Standard series

<u>SMD</u>

### V/I characteristics for standard series



CT1206K20G



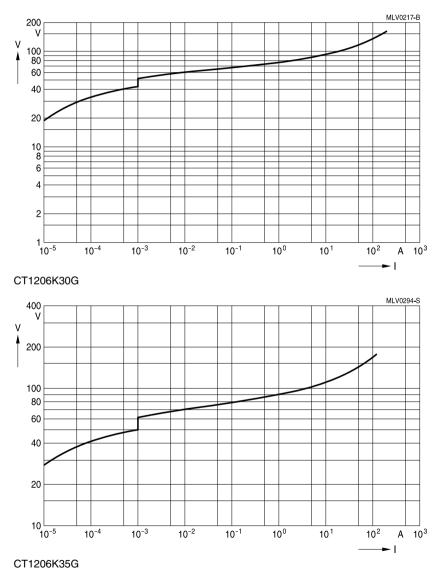
CT1206K25G



#### Standard series

<u>SMD</u>

### V/I characteristics for standard series

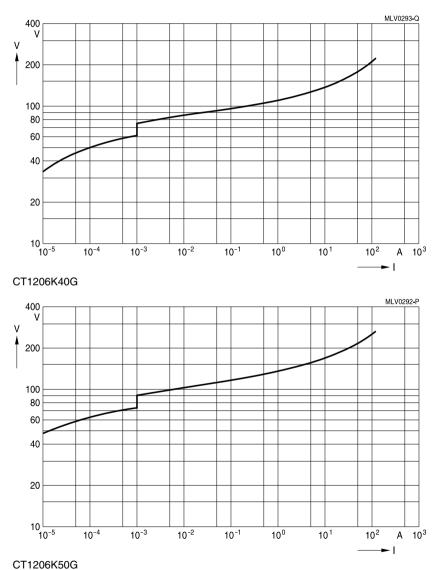




Standard series

<u>SMD</u>

#### V/I characteristics for standard series

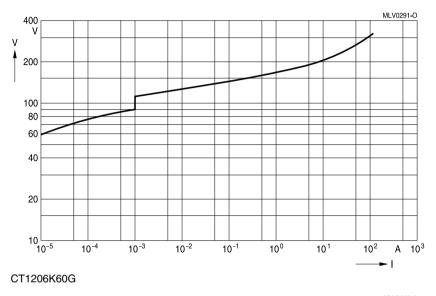


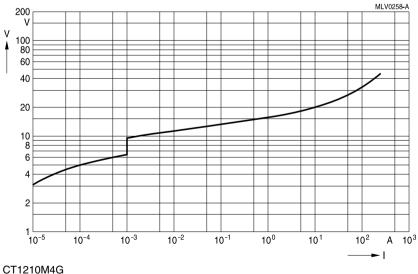


Standard series

<u>SMD</u>

#### V/I characteristics for standard series



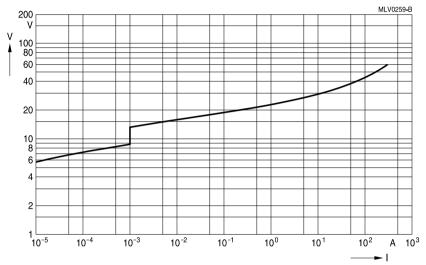




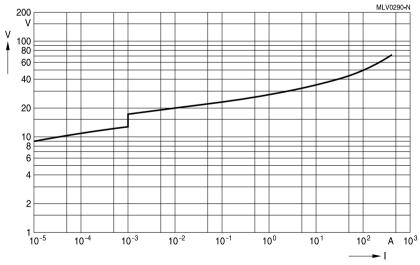
Standard series

<u>SMD</u>

### V/I characteristics for standard series



CT1210M6G



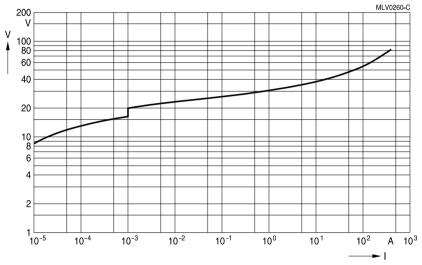
CT1210L8G



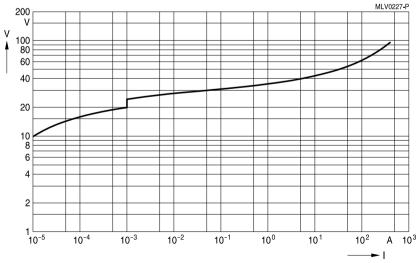
Standard series

<u>SMD</u>

### V/I characteristics for standard series



CT1210K11G



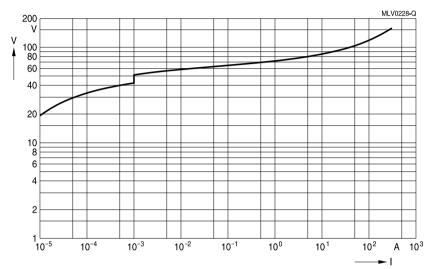
CT1210K14G



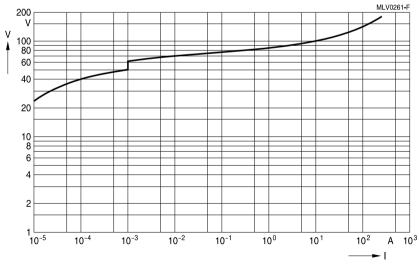
#### Standard series

<u>SMD</u>

### V/I characteristics for standard series



CT1210K30G



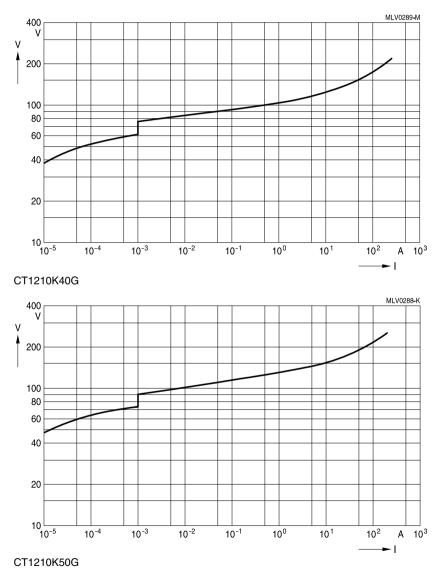
CT1210K35G



Standard series

SMD

## V/I characteristics for standard series

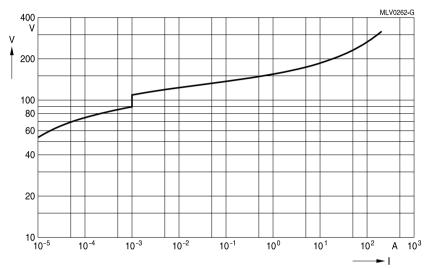




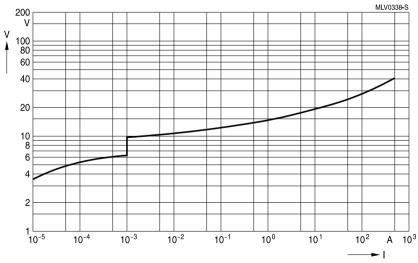
Standard series

<u>SMD</u>

## V/I characteristics for standard series



CT1210K60G



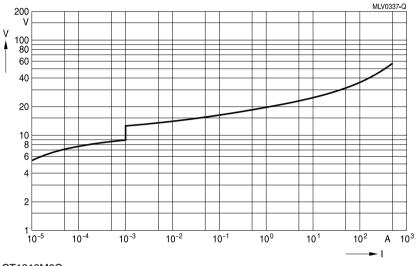
CT1812M4G



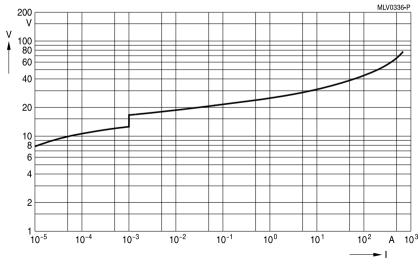
Standard series

SMD

## V/I characteristics for standard series



CT1812M6G



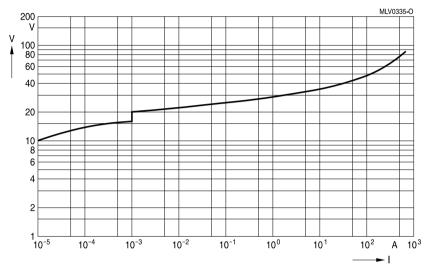
CT1812L8G



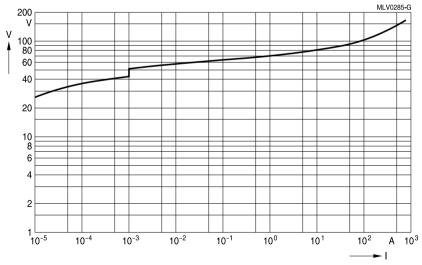
Standard series

<u>SMD</u>

## V/I characteristics for standard series



CT1812K11G



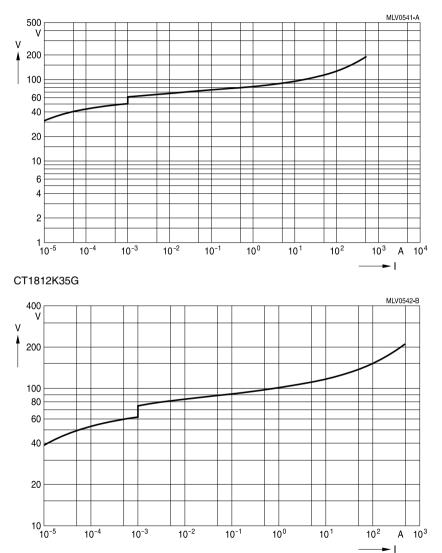
CT1812K30G



Standard series

<u>SMD</u>

## V/I characteristics for standard series



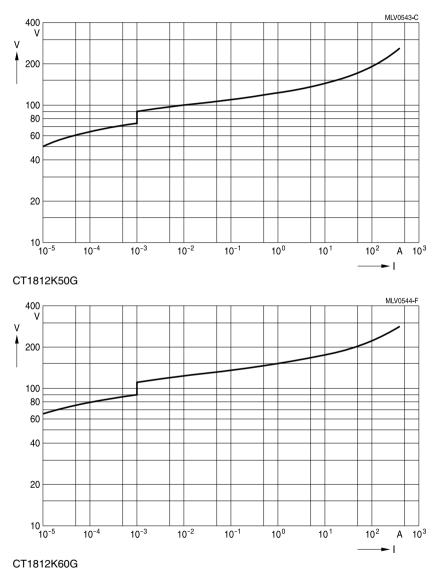
CT1812K40G



Standard series

SMD

## V/I characteristics for standard series



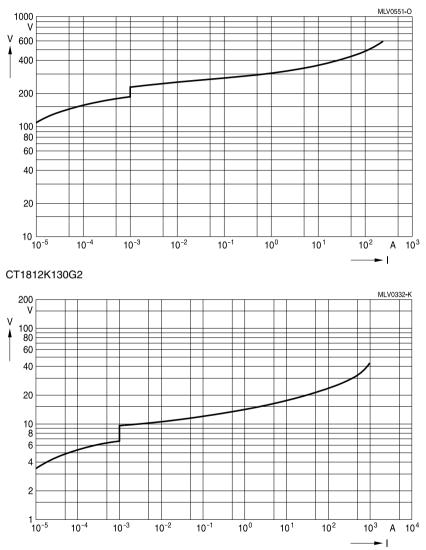
Please read *Cautions and warnings* and Downloaded from Arrow.com.



Standard series

SMD

## V/I characteristics for standard series



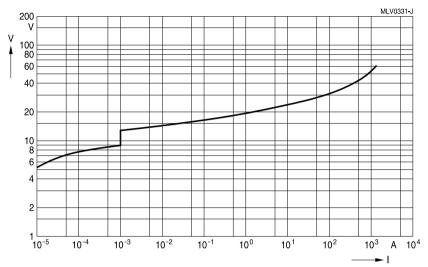
CT2220M4G



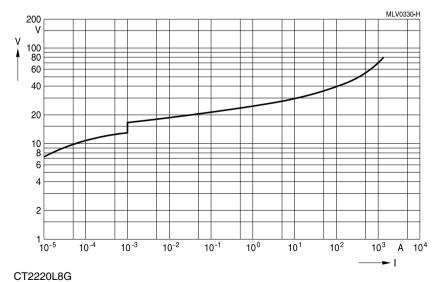
Standard series

<u>SMD</u>

## V/I characteristics for standard series



CT2220M6G

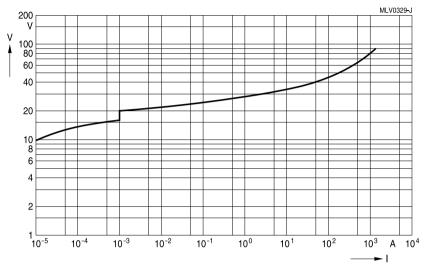




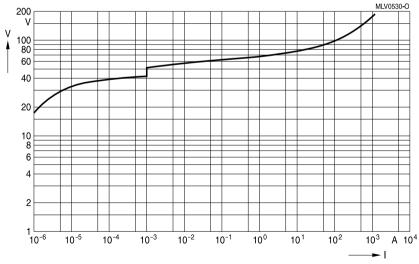
Standard series

SMD

## V/I characteristics for standard series



CT2220K11G



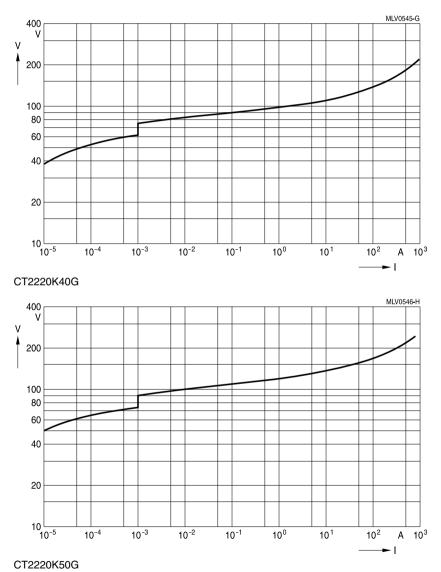
CT2220K30G



Standard series

<u>SMD</u>

## V/I characteristics for standard series

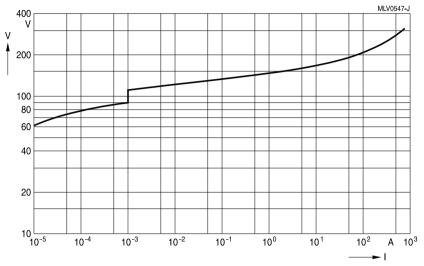




Standard series

<u>SMD</u>

## V/I characteristics for standard series



CT2220K60G



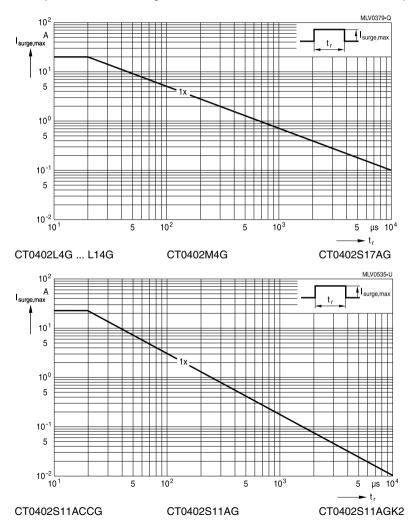
#### Standard series

## <u>SMD</u>

## **Derating curves**

Not specified for following types: CT0201S4AHSG, CT0402S14AHSG, CT0402S5ARFG, CT0402V150HSG, CT0402V150RFG, CT0402V275RFG, CT0402V90RFG, CT0603L25HSG, CT0603S14AHSG, CT0603S5ARFG, CT0603V150RFG.

Maximum surge current I<sub>surge,max</sub> = f (t<sub>r</sub>, pulse train)





#### Standard series

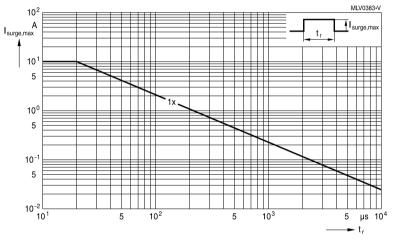
## <u>SMD</u>

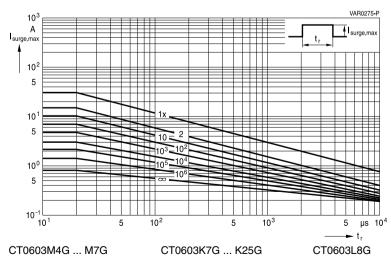
## **Derating curves**

Not specified for following types: CT0201S4AHSG, CT0402S14AHSG, CT0402S5ARFG, CT0402V150HSG, CT0402V150RFG, CT0402V275RFG, CT0402V90RFG, CT0603L25HSG, CT0603S14AHSG, CT0603S5ARFG, CT0603V150RFG.

Maximum surge current I<sub>surge,max</sub> = f (t<sub>r</sub>, pulse train)

For explanation of the derating curves refer to "General technical information", chapter 2.7.1





CT0402L14UG

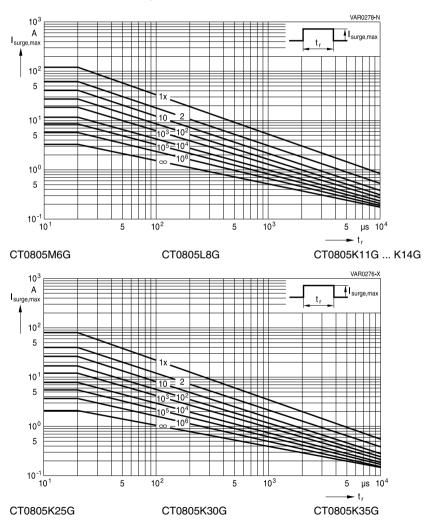


#### Standard series

# **SMD**

## **Derating curves**

Maximum surge current  $I_{surge,max} = f(t_r, pulse train)$ 



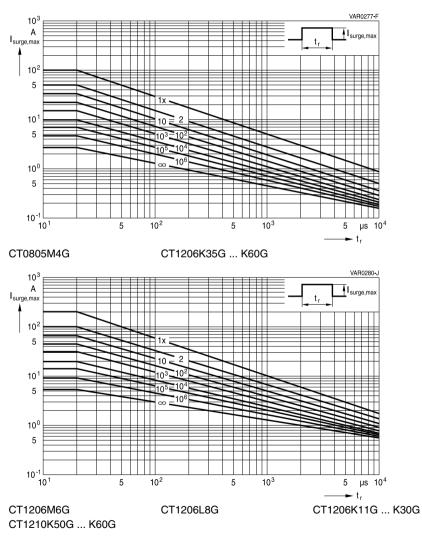


#### Standard series

# <u>SMD</u>

## **Derating curves**

Maximum surge current  $I_{surge,max} = f(t_r, pulse train)$ 



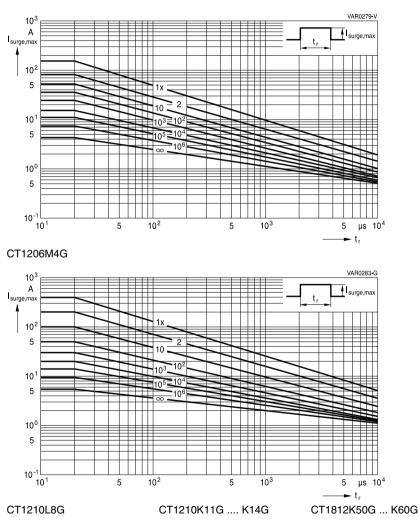


#### Standard series

# **SMD**

## **Derating curves**

Maximum surge current  $I_{surge,max} = f(t_r, pulse train)$ 



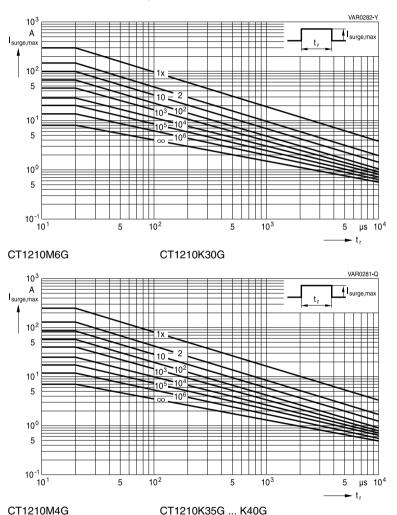


#### Standard series

# <u>SMD</u>

## **Derating curves**

Maximum surge current  $I_{surge,max} = f(t_r, pulse train)$ 





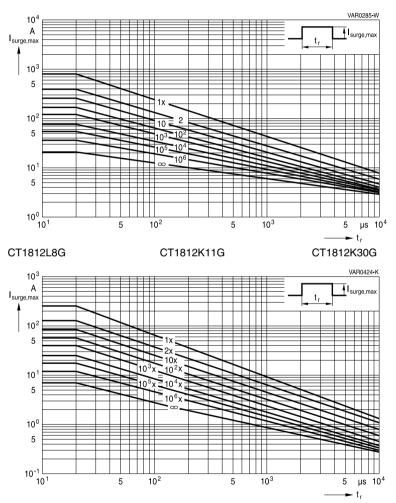
#### Standard series

# **SMD**

## **Derating curves**

Maximum surge current  $I_{surge,max} = f(t_r, pulse train)$ 

For explanation of the derating curves refer to "General technical information", chapter 2.7.1



CT1812K130G2

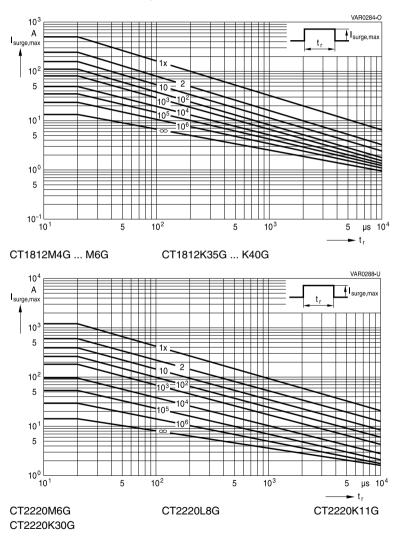


#### Standard series

# <u>SMD</u>

## **Derating curves**

Maximum surge current  $I_{surge,max} = f(t_r, pulse train)$ 





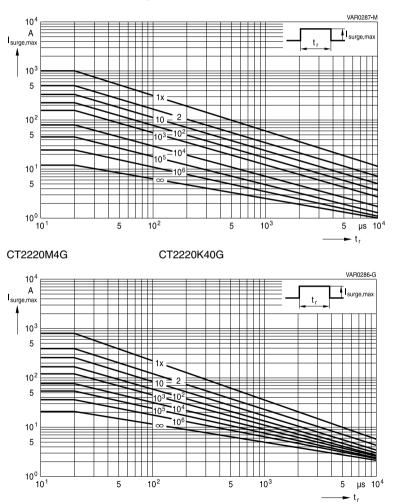
#### Standard series

# <u>SMD</u>

## **Derating curves**

Maximum surge current  $I_{surge,max} = f(t_r, pulse train)$ 

For explanation of the derating curves refer to "General technical information", chapter 2.7.1



CT2220K50G ... K60G



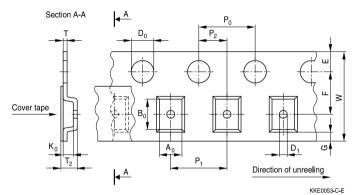
## Standard series

<u>SMD</u>

## Taping and packing

# 1 Taping and packing for SMD components

## 1.1 Blister tape (taping to IEC 60286-3)



## **Dimensions in mm**

	8-mm tape						12-mm tape	
	Case size (inch/mm)					Case size (inch/mm)		Tolerance
			0508/	0612/	1012/			
			1220	1632	2532			
	0603/	0506/	0805/	1206/	1210/	1812/	2220/	
	1608	1216	2012	3216	3225	4532	5750	
A <sub>0</sub>	0.9 ±0.10	1.50	1.50	1.80	2.80	3.50	5.10	±0.20
B <sub>0</sub>	1.75 ±0.10	1.80	2.30	3.40	3.50	4.80	6.00	±0.20
K <sub>0</sub>	1.0	0.80		1.80		3.40		max.
Т	0.30				0.30		max.	
T <sub>2</sub>	1.3	1.20	2.	50		3.90		max.
D <sub>0</sub>	1.50					1.50		+0.10/-0
D <sub>1</sub>	0.3					1.50		min.
Po			4.00			4.00		±0.101)
$P_2$			2.00			2.00		±0.05
P <sub>1</sub>	4.00					8.00		±0.10
W	8.00					12.00		±0.30
E	1.75					1	.75	±0.10
F	3.50					5	5.50	±0.05
G	0.75					C	).75	min.

1)  $\leq \pm 0.2$  mm over 10 sprocket holes.

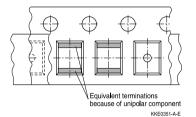


#### Standard series

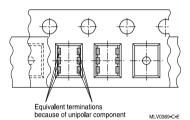
## <u>SMD</u>

## Part orientation in tape pocket for blister tape

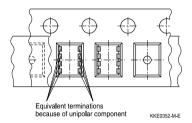
For discrete chip, EIA case sizes 0603, 0805, 1206, 1210, 1812 and 2220



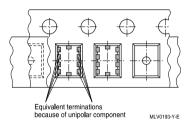
#### For arrays, EIA case sizes 0506 and 1012



## For array, EIA case size 0612



#### For filter array, EIA case size 0508



## Additional taping information

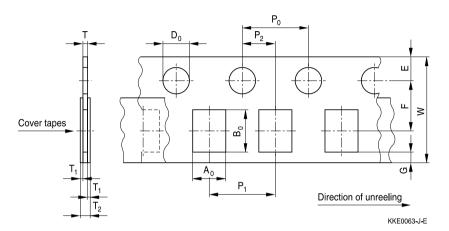
Reel material	Polystyrol (PS)
Tape material	Polystyrol (PS) or Polycarbonat (PC) or PVC
Tape break force	min. 10 N
Top cover tape strength	min. 10 N
Top cover tape peel force	0.1 to 1.0 N for 8-mm tape and 0.1 to 1.3 N for 12-mm tape at a peel speed of 300 mm/min
Tape peel angle	Angle between top cover tape and the direction of feed during peel off: $165^{\circ}$ to $180^{\circ}$
Cavity play	Each part rests in the cavity so that the angle between the part and cavity center line is no more than 20°



Standard series

<u>SMD</u>

# 1.2 Cardboard tape (taping to IEC 60286-3)



## **Dimensions in mm**

			8-mn	n tape			
		Case size (inch/mm) Case size (inch/mm)					Tolerance
	0201/0603	0402/1005	0405/1012	0603/1608	1003/2508	0508/1220	
A <sub>0</sub>	0.38 ±0.05	0.60	1.05	0.95	1.00	1.60	±0.20
B <sub>0</sub>	0.68 ±0.05	1.15	1.60	1.80	2.85	2.40	±0.20
Т	0.42 ±0.02	0.60	0.75	0.95	0.95	0.95	max.
T <sub>2</sub>	0.4 min.	0.70	0.90	1.10	1.10	1.10	max.
D <sub>0</sub>	1.50 ±0.1		1.	50		1.50	+0.10/-0
Po			4.	00			±0.10 <sup>2)</sup>
P <sub>2</sub>			2.	00			±0.05
P <sub>1</sub>	2.00 ±0.05	2.00	4.00	4.00	4.00	4.00	±0.10
W	8.00					±0.30	
E	1.75					±0.10	
F	3.50					±0.05	
G	0.75						min.

2) ≤0.2 mm over 10 sprocket holes.



#### Standard series

## <u>SMD</u>

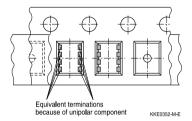
## Part orientation in tape pocket for cardboard tape

For discrete chip, EIA case sizes 0201, 0402, 0603 and 1003

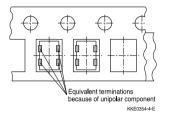
Equivalent terminations because of unipolar component



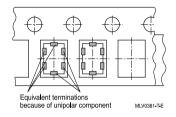
#### For array, EIA case size 0508



## For array, EIA case size 0405



#### For filter array, EIA case size 0405



## Additional taping information

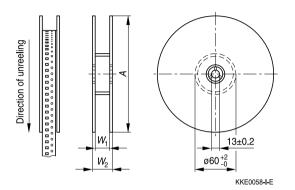
Reel material	Polystyrol (PS)
Tape material	Cardboard
Tape break force	min. 10 N
Top cover tape strength	min. 10 N
Top cover tape peel force	0.1 to 1.0 N at a peel speed of 300 mm/min
Tape peel angle	Angle between top cover tape and the direction of feed during peel off: 165° to 180° $$
Cavity play	Each part rests in the cavity so that the angle between the part and cavity center line is no more than 20°



## Standard series

<u>SMD</u>

## 1.3 Reel packing

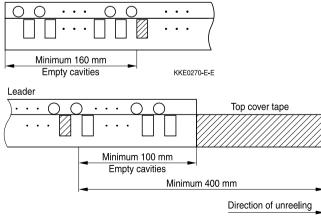


## Dimensions in mm

	8-mn	n tape	12-mm tape		
	180-mm reel	330-mm reel	180-mm reel	330-mm reel	
A	180 +0/-3	330 +0/-2.0	180 +0/-3	330 +0/-2.0	
W <sub>1</sub>	8.4 +1.5/-0	8.4 +1.5/-0	12.4 +1.5/-0	12.4 +1.5/-0	
W <sub>2</sub>	14.4 max.	14.4 max.	18.4 max.	18.4 max.	

## Leader, trailer

Trailer (tape end)



KKE0289-Q-E

Standard series

# <u>SMD</u>

## 1.4 Packing units for discrete chip and array chip

	th			180 mm	300 mm
Case size	Chip thickness	Cardboard tape	Blister tape	Ø 180-mm reel	Ø 330-mm reel
inch/mm	th	W	W	pcs.	pcs.
0201/0603	0.33 mm	8 mm	-	15000	-
0402/1005	0.6 mm	8 mm	-	10000	50000
0405/1012	0.7 mm	8 mm	-	5000	-
0506/1216	0.5 mm	-	8 mm	4000	-
0508/1220	0.9 mm	8 mm	8 mm	4000	-
0603/1608	0.9 mm	8 mm	8 mm	4000	16000
0612/1632	0.7 mm	-	8 mm	3000	-
0805/2012	0.7 mm	-	8 mm	3000	-
	0.9 mm	-	8 mm	3000	12000
	1.3 mm	-	8 mm	3000	12000
1003/2508	0.9 mm	8 mm	-	4000	_
1012/2532	1.0 mm	-	8 mm	2000	-
1206/3216	0.9 mm	-	8 mm	3000	-
	1.3 mm	-	8 mm	3000	12000
	1.4 mm	-	8 mm	2000	8000
	1.6 mm	_	8 mm	2000	8000
1210/3225	0.9 mm	-	8 mm	3000	-
	1.3 mm	-	8 mm	3000	12000
	1.4 mm	-	8 mm	2000	8000
	1.6 mm	-	8 mm	2000	8000
1812/4532	1.3 mm	-	12 mm	1500	-
	1.4 mm	-	12 mm	1000	-
	1.6 mm	-	12 mm	1000	4000
	2.0 mm	-	12 mm	-	3000
	2.3 mm	-	12 mm	-	3000
2220/5750	1.3 mm	-	12 mm	1500	-
	1.4 mm	—	12 mm	1000	-
	1.6 mm	—	12 mm	1000	-
	2.0 mm	—	12 mm	-	3000
	2.3 mm	-	12 mm	-	3000
	2.7 mm	—	12 mm	600	—
	3.0 mm	-	12 mm	600	—



## Standard series

# <u>SMD</u>

## 2 Delivery mode for leaded SHCV varistors

Standard delivery mode for SHCV types is bulk. Alternative taping modes (AMMO pack or taped on reel) are available upon request.

Packing units for:

Туре	Pieces	
SR6	2000	
SR1 / SR2	1000	

For types not listed in this data book please contact EPCOS.



Standard series

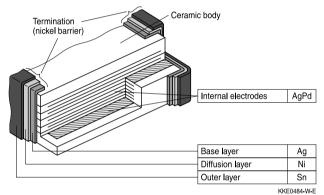
SMD

#### Soldering directions

#### 1 Terminations and soldering methods

#### 1.1 Nickel barrier termination

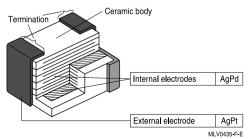
The nickel barrier layer of the silver/nickel/tin termination prevents leaching of the silver base metallization layer. This allows great flexibility in the selection of soldering parameters. The tin prevents the nickel layer from oxidizing and thus ensures better wetting by the solder. The nickel barrier termination is suitable for lead-free soldering, as well as for other commonly-used soldering methods.



Multilayer CTVS: Structure of nickel barrier termination

## 1.2 Silver-platinum termination

Silver-platinum terminations are mainly used for the large EIA case sizes 1812 and 2220. The silver-platinum termination is approved for reflow soldering, SnPb soldering and lead-free soldering with a silver containing solder paste. In case of SnPb soldering, a solder paste Sn62Pb36Ag2 is recommended. For lead-free reflow soldering, a solder paste SAC, e.g. Sn95.5Ag3.8Cu0.7, is recommended.



Multilayer varistor: Structure of silver-platinum termination



#### Standard series

## <u>SMD</u>

## 1.3 Silver-palladium termination

Silver-palladium terminations are designed for the use of conductive adhesivs. Lead-free reflow soldering does not form a proper solder joint. In general reflow or wave soldering is not recommended.

## 1.4 Tinned iron wire

All SHCV types with tinned terminations are suitable for lead-free and SnPb soldering.



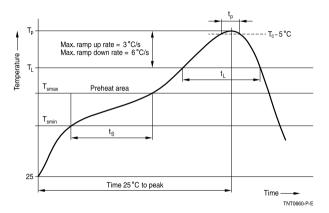
#### Standard series

## **SMD**

## 2 Recommended soldering temperature profiles

#### 2.1 Reflow soldering temperature profile

Temperature ranges for reflow soldering acc. to IEC 60068-2-58 recommendations.



Profile feature		Sn-Pb eutectic assembly	Pb-free assembly
Preheat and soak			
- Temperature min	$T_{smin}$	100 °C	150 °C
- Temperature max	T <sub>smax</sub>	150 °C	200 °C
- Time	$t_{\mbox{smin}}$ to $t_{\mbox{smax}}$	60 120 s	60 120 s
Average ramp-up rate	$T_{\text{smax}}$ to $T_{\text{p}}$	3 °C/ s max.	3 °C/ s max.
Liquidous temperature	TL	183 °C	217 °C
Time at liquidous	tL	40 150 s	40 150 s
Peak package body temperature	T <sub>p</sub>	215 °C 260 °C <sup>1)</sup>	235 °C 260 °C
Time above (T <sub>P</sub> $-5$ °C)	t <sub>p</sub>	10 40 s	10 40 s
Average ramp-down rate	$T_p$ to $T_{smax}$	6 °C/ s max.	6 °C/ s max.
Time 25 °C to peak temperature		max. 8 minutes	max. 8 minutes

1) Depending on package thickness.

# **Notes:** All temperatures refer to topside of the package, measured on the package body surface.

Number of reflow cycles: 3

Iron soldering should be avoided, hot air methods are recommended for repair purposes.

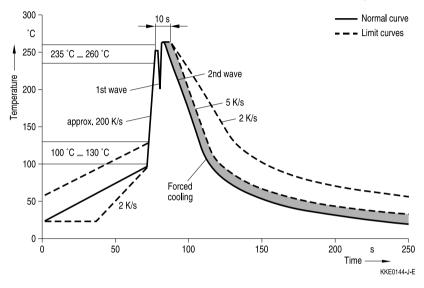


## Standard series

## <u>SMD</u>

## 2.2 Wave soldering temperature profile

Temperature characteristics at component terminal with dual-wave soldering



## 3 Solder joint profiles / solder quantity

## 3.1 Nickel barrier termination

If the meniscus height is too low, that means the solder quantity is too low, the solder joint may break, i.e. the component becomes detached from the joint. This problem is sometimes interpreted as leaching of the external terminations.

If the solder meniscus is too high, i.e. the solder quantity is too large, the vise effect may occur. As the solder cools down, the solder contracts in the direction of the component. If there is too much solder on the component, it has no leeway to evade the stress and may break, as in a vise.

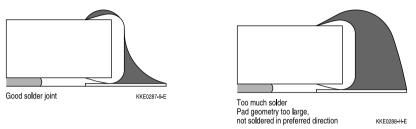
The figures below show good and poor solder joints for dual-wave and infrared soldering.



Standard series

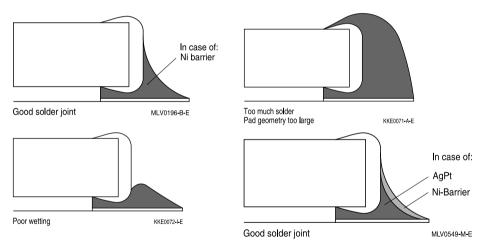
**SMD** 

## 3.1.1 Solder joint profiles for nickel barrier termination - dual-wave soldering



Good and poor solder joints caused by amount of solder in dual-wave soldering.

# 3.1.2 Solder joint profiles for nickel barrier termination / silver-platinum termination - reflow soldering



Good and poor solder joints caused by amount of solder in reflow soldering.



Standard series

# <u>SMD</u>

## 4 Solderability tests

Test	Standard	Test conditions Sn-Pb soldering	Test conditions Pb-free soldering	Criteria/ test results
Wettability	IEC 60068-2-58	Immersion in 60/40 SnPb solder using non-activated flux at 215 ±3 °C for 3 ±0.3 s	Immersion in Sn96.5Ag3.0Cu0.5 solder using non- or low activated flux at 245 ±5 °C for 3 ±0.3 s	Covering of 95% of end termination, checked by visual inspection
Leaching resistance	IEC 60068-2-58	Immersion in 60/40 SnPb solder using mildly activated flux without preheating at 260 $\pm$ 5 °C for 10 $\pm$ 1 s	Immersion in Sn96.5Ag3.0Cu0.5 solder using non- or low activated flux without preheating at $255 \pm 5$ °C for $10 \pm 1$ s	No leaching of contacts
Thermal shock (solder shock)		Dip soldering at 300 °C/5 s	Dip soldering at 300 °C/5 s	No deterioration of electrical parameters. Capacitance change: $ \Delta C/C_0  \le 15\%$
Tests of resistance to soldering heat for SMDs		Immersion in 60/40 SnPb for 10 s at 260 °C	Immersion in Sn96.5Ag3.0Cu0.5 for 10 s at 260 °C	Change of varistor voltage: $ \Delta V/V (1 \text{ mA})  \le 5\%$
Tests of resistance to soldering heat for radial leaded components (SHCV)	IEC 60068-2-20	Immersion of leads in 60/40 SnPb for 10 s at 260 °C	Immersion of leads in Sn96.5Ag3.0Cu0.5 for 10 s at 260 °C	Change of varistor voltage: $ \Delta V/V (1 \text{ mA})  \le 5\%$ Change of capacitance X7R: $\le -5/+10\%$



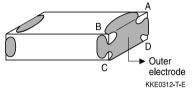
#### Standard series

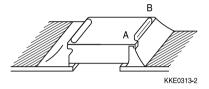
<u>SMD</u>

## Note:

## Leaching of the termination

Effective area at the termination might be lost if the soldering temperature and/or immersion time are not kept within the recommended conditions. Leaching of the outer electrode should not exceed 25% of the chip end area (full length of the edge A-B-C-D) and 25% of the length A-B, shown below as mounted on substrate.





As mounted on substrate

As a single chip

#### 5 Notes for proper soldering

#### 5.1 Preheating and cooling

According to IEC 60068-2-58. Please refer to section 2 of this chapter.

## 5.2 Repair/ rework

Manual soldering with a soldering iron must be avoided, hot-air methods are recommended for rework purposes.

## 5.3 Cleaning

All environmentally compatible agents are suitable for cleaning. Select the appropriate cleaning solution according to the type of flux used. The temperature difference between the components and cleaning liquid must not be greater than 100 °C. Ultrasonic cleaning should be carried out with the utmost caution. Too high ultrasonic power can impair the adhesive strength of the metal-lized surfaces.

## 5.4 Solder paste printing (reflow soldering)

An excessive application of solder paste results in too high a solder fillet, thus making the chip more susceptible to mechanical and thermal stress. Too little solder paste reduces the adhesive strength on the outer electrodes and thus weakens the bonding to the PCB. The solder should be applied smoothly to the end surface.



Standard series

<u>SMD</u>

## 5.5 Selection of flux

Used flux should have less than or equal to 0.1 wt % of halogenated content, since flux residue after soldering could lead to corrosion of the termination and/or increased leakage current on the surface of the component. Strong acidic flux must not be used. The amount of flux applied should be carefully controlled, since an excess may generate flux gas, which in turn is detrimental to solderability.

## 5.6 Storage of CTVSs

Solderability is guaranteed for one year from date of delivery for multilayer varistors, CeraDiodes and ESD/EMI filters (half a year for chips with AgPt terminations) and two years for SHCV components, provided that components are stored in their original packages.

Storage temperature: -25 °C to +45 °C

Relative humidity: ≤75% annual average, ≤95% on 30 days a year

The solderability of the external electrodes may deteriorate if SMDs and leaded components are stored where they are exposed to high humidity, dust or harmful gas (hydrogen chloride, sulfurous acid gas or hydrogen sulfide).

Do not store SMDs and leaded components where they are exposed to heat or direct sunlight. Otherwise the packing material may be deformed or SMDs/ leaded components may stick together, causing problems during mounting.

After opening the factory seals, such as polyvinyl-sealed packages, it is recommended to use the SMDs or leaded components as soon as possible.

Solder CTVS components after shipment from TDK Electronics within the time specified:

CTVS with Ni barrier termination:	12 months
CTVS with AgPt termination:	6 months
SHCV (leaded components):	24 months

## 5.7 Placement of components on circuit board

Especially in the case of dual-wave soldering, it is of advantage to place the components on the board before soldering in that way that their two terminals do not enter the solder bath at different times.

Ideally, both terminals should be wetted simultaneously.



Standard series

<u>SMD</u>

## 5.8 Soldering cautions

An excessively long soldering time or high soldering temperature results in leaching of the outer electrodes, causing poor adhesion and a change of electrical properties of the varistor due to the loss of contact between electrodes and termination.

Keep the recommended down-cooling rate.

## 5.9 Standards

CECC 00802 IEC 60068-2-58 IEC 60068-2-20



Standard series

# <u>SMD</u>

## Symbols and terms

## For ceramic transient voltage suppressors (CTVS)

Symbol	Term
$C_{\text{line,max}}$	Maximum capacitance per line
$\mathbf{C}_{\text{line,min}}$	Minimum capacitance per line
$C_{\text{line,typ}}$	Typical capacitance per line
C <sub>max</sub>	Maximum capacitance
$C_{\text{min}}$	Minimum capacitance
$C_{nom}$	Nominal capacitance
$\Delta \bm{C}_{\text{nom}}$	Tolerance of nominal capacitance
C <sub>typ</sub>	Typical capacitance
$\mathbf{f}_{cut-off,max}$	Maximum cut-off frequency
$\mathbf{f}_{cut-off,min}$	Minimum cut-off frequency
$\mathbf{f}_{cut-off,typ}$	Typical cut-off frequency
$\mathbf{f}_{res,typ}$	Typical resonance frequency
I	Current
I <sub>clamp</sub>	Clamping current
l <sub>leak</sub>	Leakage current
I <sub>leak,max</sub>	Maximum leakage current
leak,typ	Typical leakage current
I <sub>PP</sub>	Peak pulse current
I <sub>surge,max</sub>	Maximum surge current (also termed peak current)
LCT	Lower category temperature
L <sub>typ</sub>	Typical inductance
$P_{\text{diss},\text{max}}$	Maximum power dissipation
P <sub>PP</sub>	Peak pulse power
R <sub>ins</sub>	Insulation resistance
$R_{min}$	Minimum resistance
Rs	Resistance per line
$R_{S,typ}$	Typical resistance per line
T <sub>A</sub>	Ambient temperature
$T_{op}$	Operating temperature
$T_{op,max}$	Maximum operating temperature
T <sub>stg</sub>	Storage temperature



## Standard series

# <u>SMD</u>

Symbol	Term
t <sub>r</sub>	Duration of equivalent rectangular wave
t <sub>resp</sub>	Response time
t <sub>resp,max</sub>	Maximum response time
UCT	Upper category temperature
V	Voltage
$V_{\text{BR,min}}$	Minimum breakdown voltage
$V_{\text{clamp,max}}$	Maximum clamping voltage
$V_{\text{DC,max}}$	Maximum DC operating voltage (also termed working voltage)
$V_{\text{ESD,air}}$	Air discharge ESD capability
$V_{\text{ESD,contact}}$	Contact discharge ESD capability
$V_{jump}$	Maximum jump-start voltage
$V_{RMS,max}$	Maximum AC operating voltage, root-mean-square value
Vv	Varistor voltage (also termed breakdown voltage)
$V_{LD}$	Maximum load dump voltage
V <sub>leak</sub>	Measurement voltage for leakage current
$V_{V,min}$	Minimum varistor voltage
$V_{v,max}$	Maximum varistor voltage
$\Delta V_{v}$	Tolerance of varistor voltage
W <sub>LD</sub>	Maximum load dump energy
$W_{\text{max}}$	Maximum energy absorption (also termed transient energy)
$lpha_{typ}$	Typical insertion loss
tan δ	Dissipation factor
е	Lead spacing
<u></u>	Maximum possible application conditions

All dimensions are given in mm.

The commas used in numerical values denote decimal points.



Standard series

SMD

## Cautions and warnings

#### General

Some parts of this publication contain statements about the suitability of our ceramic transient voltage suppressor (CTVS) components (multilayer varistors (MLVs)), CeraDiodes, ESD/EMI filters, leaded transient voltage/ RFI suppressors (SHCV types)) for certain areas of application, including recommendations about incorporation/design-in of these products into customer applications. The statements are based on our knowledge of typical requirements often made of our CTVS devices in the particular areas. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our CTVS components for a particular customer application. As a rule, EPCOS is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always incumbent on the customer to check and decide whether the CTVS devices with the properties described in the product specification are suitable for use in a particular customer application.

- Do not use EPCOS CTVS components for purposes not identified in our specifications, application notes and data books.
- Ensure the suitability of a CTVS in particular by testing it for reliability during design-in. Always evaluate a CTVS component under worst-case conditions.
- Pay special attention to the reliability of CTVS devices intended for use in safety-critical applications (e.g. medical equipment, automotive, spacecraft, nuclear power plant).

#### **Design notes**

- Always connect a CTVS in parallel with the electronic circuit to be protected.
- Consider maximum rated power dissipation if a CTVS has insufficient time to cool down between a number of pulses occurring within a specified isolated time period. Ensure that electrical characteristics do not degrade.
- Consider derating at higher operating temperatures. Choose the highest voltage class compatible with derating at higher temperatures.
- Surge currents beyond specified values will puncture a CTVS. In extreme cases a CTVS will burst.
- If steep surge current edges are to be expected, make sure your design is as low-inductance as possible.
- In some cases the malfunctioning of passive electronic components or failure before the end of their service life cannot be completely ruled out in the current state of the art, even if they are operated as specified. In applications requiring a very high level of operational safety and especially when the malfunction or failure of a passive electronic component could endanger human life or health (e.g. in accident prevention, life-saving systems, or automotive battery line applications such as clamp 30), ensure by suitable design of the application or other measures (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of such a malfunction or failure. Only use CTVS components from the automotive series in safety-relevant applications.



Standard series

## SMD

Specified values only apply to CTVS components that have not been subject to prior electrical, mechanical or thermal damage. The use of CTVS devices in line-to-ground applications is therefore not advisable, and it is only allowed together with safety countermeasures like thermal fuses.

## Storage

- Only store CTVS in their original packaging. Do not open the package prior to processing.
- Storage conditions in original packaging: temperature -25 to +45°C, relative humidity ≤75% annual average, maximum 95%, dew precipitation is inadmissible.
- Do not store CTVS devices where they are exposed to heat or direct sunlight. Otherwise the packaging material may be deformed or CTVS may stick together, causing problems during mounting.
- Avoid contamination of the CTVS surface during storage, handling and processing.
- Avoid storing CTVS devices in harmful environments where they are exposed to corrosive gases for example (SO<sub>x</sub>, CI).
- Use CTVS as soon as possible after opening factory seals such as polyvinyl-sealed packages.
- Solder CTVS components after shipment from EPCOS within the time specified:
  - CTVS with Ni barrier termination, 12 months
  - CTVS with AgPt termination, 6 months
  - SHCV, 24 months

## Handling

- Do not drop CTVS components and allow them to be chipped.
- Do not touch CTVS with your bare hands gloves are recommended.
- Avoid contamination of the CTVS surface during handling.
- Washing processes may damage the product due to the possible static or cyclic mechanical loads (e.g. ultrasonic cleaning). They may cause cracks to develop on the product and its parts, which might lead to reduced reliability or lifetime.

## Mounting

- When CTVS devices are encapsulated with sealing material or overmolded with plastic material, electrical characteristics might be degraded and the life time reduced.
- Make sure an electrode is not scratched before, during or after the mounting process.
- Make sure contacts and housings used for assembly with CTVS components are clean before mounting.
- The surface temperature of an operating CTVS can be higher. Ensure that adjacent components are placed at a sufficient distance from a CTVS to allow proper cooling.
- Avoid contamination of the CTVS surface during processing.



#### Standard series

## SMD

## Soldering

- Complete removal of flux is recommended to avoid surface contamination that can result in an instable and/or high leakage current.
- Use resin-type or non-activated flux.
- Bear in mind that insufficient preheating may cause ceramic cracks.
- Rapid cooling by dipping in solvent is not recommended, otherwise a component may crack.

## Operation

- Use CTVS only within the specified operating temperature range.
- Use CTVS only within specified voltage and current ranges.
- Environmental conditions must not harm a CTVS. Only use them in normal atmospheric conditions. Reducing the atmosphere (e.g. hydrogen or nitrogen atmosphere) is prohibited.
- Prevent a CTVS from contacting liquids and solvents. Make sure that no water enters a CTVS (e.g. through plug terminals).
- Avoid dewing and condensation.
- EPCOS CTVS components are mainly designed for encased applications. Under all circumstances avoid exposure to:
  - direct sunlight
  - rain or condensation
  - steam, saline spray
  - corrosive gases
  - atmosphere with reduced oxygen content
- EPCOS CTVS devices are not suitable for switching applications or voltage stabilization where static power dissipation is required.

This listing does not claim to be complete, but merely reflects the experience of EPCOS AG.

#### **Display of ordering codes for EPCOS products**

The ordering code for one and the same EPCOS product can be represented differently in data sheets, data books, other publications, on the EPCOS website, or in order-related documents such as shipping notes, order confirmations and product labels. **The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products**. Detailed information can be found on the Internet under www.epcos.com/orderingcodes



The following applies to all products named in this publication:

- 1. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule, we are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether a product with the properties described in the product specification is suitable for use in a particular customer application.
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- 3. The warnings, cautions and product-specific notes must be observed.
- 4. In order to satisfy certain technical requirements, some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as hazardous). Useful information on this will be found in our Material Data Sheets on the Internet (www.tdk-electronics.tdk.com/material). Should you have any more detailed questions, please contact our sales offices.
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- 6. Unless otherwise agreed in individual contracts, all orders are subject to our General Terms and Conditions of Supply.



## Important notes

- 7. Our manufacturing sites serving the automotive business apply the IATF 16949 standard. The IATF certifications confirm our compliance with requirements regarding the quality management system in the automotive industry. Referring to customer requirements and customer specific requirements ("CSR") TDK always has and will continue to have the policy of respecting individual agreements. Even if IATF 16949 may appear to support the acceptance of unilateral requirements, we hereby like to emphasize that only requirements mutually agreed upon can and will be implemented in our Quality Management System. For clarification purposes we like to point out that obligations from IATF 16949 shall only become legally binding if individually agreed upon.
- 8. The trade names EPCOS, CeraCharge, CeraDiode, CeraLink, CeraPad, CeraPlas, CSMP, CTVS, DeltaCap, DigiSiMic, ExoCore, FilterCap, FormFit, LeaXield, MiniBlue, MiniCell, MKD, MKK, MotorCap, PCC, PhaseCap, PhaseCube, PhaseMod, PhiCap, PowerHap, PQSine, PQvar, SIFERRIT, SIFI, SIKOREL, SilverCap, SIMDAD, SiMic, SIMID, SineFormer, SIOV, ThermoFuse, WindCap are trademarks registered or pending in Europe and in other countries. Further information will be found on the Internet at www.tdk-electronics.tdk.com/trademarks.

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