



DLPA004

DATA BUS TRANSIENT SUPPRESSOR

Features

- Fast Switching Speed
- Ultra-Small Surface Mount Package
- Lead Free By Design/RoHS Compliant (Note 3)
- "Green" Device (Note 4)
- Qualified to AEC-Q101 Standards for High Reliability

Data Line Transient Protection

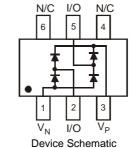
In accordance with (Note 1):

- IEC 61000-4-2 Contact Method: ±15kV
- IEC 61000-4-2 Air Discharge Method: ±25kV

Mechanical Data

- Case: SOT-363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0 (Note 3)
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208
- Ordering Information: See Page 3
- Marking Information: See Page 3
- Weight: 0.006 grams (approximate)







TOP VIEW

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _R WM V _R	85	V	
RMS Reverse Voltage		$V_{R(RMS)}$	60	V
Forward Current (Single Diode)		I _{FM}	200	mA
Peak Forward Surge Current 8.3ms Single half Sine-Wave Superimposed on Rate	I _{FM(surge)}	3.5	А	
Average Rectified Forward Current (Note 1)	I _{F(AV)}	1	А	
Repetitive Peak Forward Current		I _{FRM}	450	mA
@	2 t = 1.0μs 2 t = 1.0ms 2 t = 1.0s	I _{FSM}	4.0 1.0 0.5	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 2)	P _D	200	mW
Thermal Resistance Junction to Ambient Air (Note 2)	$R_{ heta JA}$	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Notes:

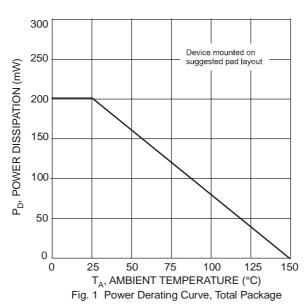
- 1. Tested with V_{CC} pins connected to GND pin.
- 2. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- 3. No purposefully added lead.
- 4. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

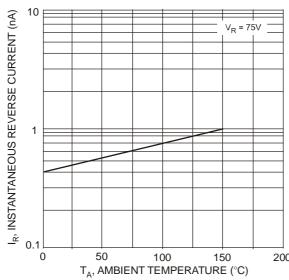


Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 5)	V _{(BR)R}	85	_	_	V	$I_R = 100 \mu A$
Forward Voltage	V _F		_	0.80 0.90 1.0 1.25	V	I _F = 1.0mA I _F = 10mA I _F = 50mA I _F = 150mA
Leakage Current (Note 5)	I _R		_ _ _	2.5 30 50	μА	$V_R = 70V$ $V_R = 25V$, $T_J = 150$ °C $V_R = 70V$, $T_J = 150$ °C
Total Capacitance (per element)	C _T	_	2	_	pF	$V_R = 0, f = 1.0MHz$
Capacitance Between Two Data Lines (DL ₁ & DL ₂ , DL ₁ & DL ₃)	C_{LL}	_	1.6	2.0	pF	$V_R = 0, f = 1.0MHz$
Capacitance Between Data Line and Ground	C_{LG}	_	2.3	3.0	pF	$V_R = 0$, $f = 1.0MHz$
Reverse Recovery Time	t _{rr}	_	_	3.0	μЅ	$I_F = I_R = 10 \text{mA},$ $I_{rr} = 0.1 \text{ x } I_R, R_L = 100 \Omega$

Notes: 5. Short duration pulse test used to minimize self-heating effect.





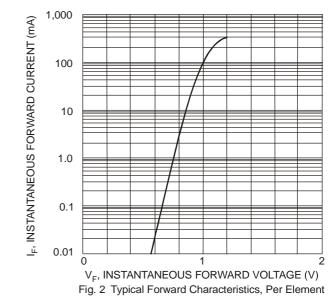


Fig. 3 Typical Reverse Characteristics, Per Element



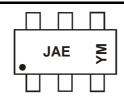
Ordering Information (Note 6)

Part Number	Case	Packaging
DLPA004-7	SOT-363	3000/Tape & Reel

Notes:

6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

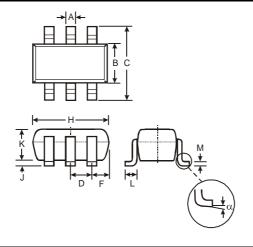


JAE = Product Type Marking Code YM = Date Code Marking Y = Year (ex: V = 2008) M = Month (ex: 9 = September)

Date Code Key

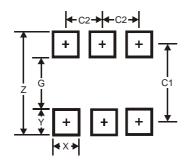
Bate Code Rey												
Year	2008		2009	2010		2011	2012		2013	2014		2015
Code	V		W	X		Υ	Z		Α	В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Package Outline Dimensions



SOT-363					
Dim	Min	Max			
Α	0.10	0.30			
В	1.15	1.35			
С	2.00	2.20			
D	0.65 Typ				
F	0.40	0.45			
Н	1.80	2.20			
J	0	0.10			
K	0.90	1.00			
L	0.25	0.40			
М	0.10	0.22			
α	0°	8°			
All Dimensions in mm					

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Y	0.6
C1	1.9
C2	0.65



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