



BAW56

DUAL SURFACE MOUNT SWITCHING DIODE

Features

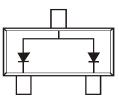
- Fast Switching Speed
- Surface Mount Package Ideally Suited for Automated Insertion
- For General Purpose Switching Applications
- Two Diode Elements Connected in a Common Anode Configuration
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Polarity: See Diagram
- Weight: 0.008 grams (Approximate)







Top View Internal Schematic

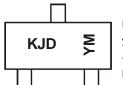
Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
BAW56-7-F	Standard	SOT23	3,000/Tape & Reel
BAW56-13-F	Standard	SOT23	10,000/Tape & Reel
BAW56Q-7-F	Automotive	SOT23	3,000/Tape & Reel
BAW56Q-13-F	Automotive	SOT23	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3).compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

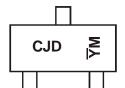
Marking Information



K = SAT (Shanghai Assembly / Test site) JD = Product Type Marking Code YM = Date Code Marking

Y = Year (ex: F = 2018)

M = Month (ex: 9 = September)



C = CAT (Chengdu Assembly / Test site)
JD = Product Type Marking Code

YM = Date Code Marking for Chengdu

 \overline{Y} = Year (ex: F = 2018)

M = Month (ex: 9 = September)

Date Code Kev

Date Code Ite	-)														
Year	1998	1999	2000		2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021
Code	J	K	L		Υ	Z	Α	В	С	D	Е	F	G	Н	I
Month	Jan	Fel	b	Mar	Apr	May	Ju	n	Jul	Aug	Sep	Oc	t I	Nov	Dec
Code	1	2		3	4	5	6		7	8	9	0		N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Non-Repetitive Peak Reverse Voltage		V_{RM}	100	V
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		Vrrm Vrwm Vr	75	V
RMS Reverse Voltage		V _{R(RMS)}	53	V
Forward Continuous Current (Note 5)		I _{FM}	300	mA
Non-Repetitive Peak Forward Surge Current	@ t = 1.0µs @ t = 1.0s	I _{FSM}	2.0 1.0	А

Thermal Characteristics

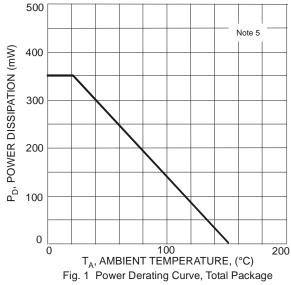
Characteristic	Symbol	Value	Unit
Typical Power Dissipation (Note 5)	P_{D}	350	mW
Typical Thermal Resistance Junction to Ambient Air (Note 5)	$R_{ heta JA}$	357	°C/W
Operating and Storage Temperature Range	T_J,T_STG	-65 to +150	°C

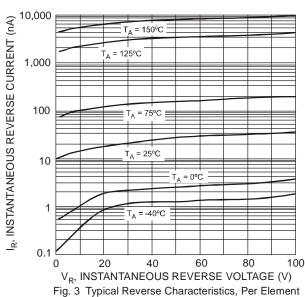
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

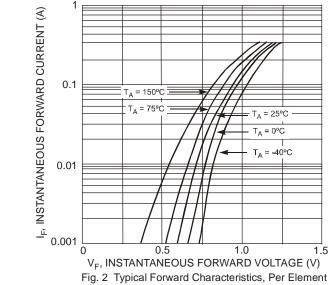
Characteristic	Symbol	Min	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	$V_{(BR)R}$	75	_	V	$I_R = 2.5\mu A$
Forward Voltage	V _F	_	0.715 0.855 1.0 1.25	V	I _F = 1.0mA I _F = 10mA I _F = 50mA I _F = 150mA
Reverse Current (Note 6)	I _R	_	2.5 50 30 25	μΑ μΑ μΑ nA	$V_R = 75V$ $V_R = 75V$, $T_J = +150$ °C $V_R = 25V$, $T_J = +150$ °C $V_R = 20V$
Total Capacitance	C _T	_	2.0	pF	V _R = 0, f = 1.0MHz
Reverse Recovery Time	t _{rr}	_	4.0	ns	$I_F = I_R = 10 \text{mA},$ $I_{rr} = 0.1 \times I_R, R_L = 100 \Omega$

5. Part mounted on FR-4 substrate PC board with 1inch squared, 2oz copper pad layout.6. Short duration pulse test used to minimize self-heating effect. Notes:









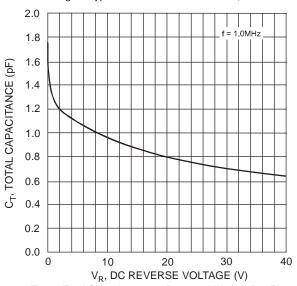
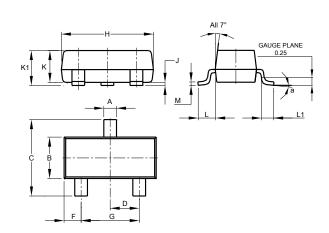


Fig. 4 Total Capacitance vs. Reverse Voltage, Per Element

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



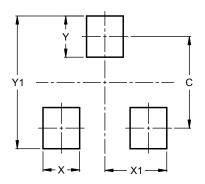
SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
C	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
7	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	a 0° 8°						
All	All Dimensions in mm						



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9

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