

**CBRLDSH2-100**  
**SURFACE MOUNT**  
**LOW PROFILE**  
**2 AMP SILICON**  
**SCHOTTKY BRIDGE RECTIFIER**



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**DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CBRLDSH2-100 is a full wave Schottky bridge rectifier mounted in a low profile epoxy surface mount molded case, utilizing glass passivated chips.

**MARKING CODE: CLP2**

**APPLICATIONS:**

- Solid state lighting (SSL)
- DC-DC converters
- Polarity protection

**FEATURES:**

- Low leakage current (0.25 $\mu$ A TYP @ 100V)
- Low profile case (1.45mm MAX)
- Low  $V_F$  Schottky diodes (770mV TYP @  $I_F=2.0A$ )

**MAXIMUM RATINGS:** ( $T_A=25^\circ C$  unless otherwise noted)

	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	$V_{RRM}$	100	V
DC Blocking Voltage	$V_R$	100	V
RMS Reverse Voltage	$V_{R(RMS)}$	70	V
Average Forward Current ( $T_L=50^\circ C$ )	$I_O$	2.0	A
Peak Forward Surge Current ( $t_p=8.3ms$ )	$I_{FSM}$	50	A
Operating Junction Temperature	$T_J$	-55 to +125	$^\circ C$
Storage Temperature	$T_{stg}$	-55 to +150	$^\circ C$
Thermal Resistance (Note 1)	$\Theta_{JA}$	95	$^\circ C/W$

**ELECTRICAL CHARACTERISTICS PER DIODE:** ( $T_A=25^\circ C$  unless otherwise noted)

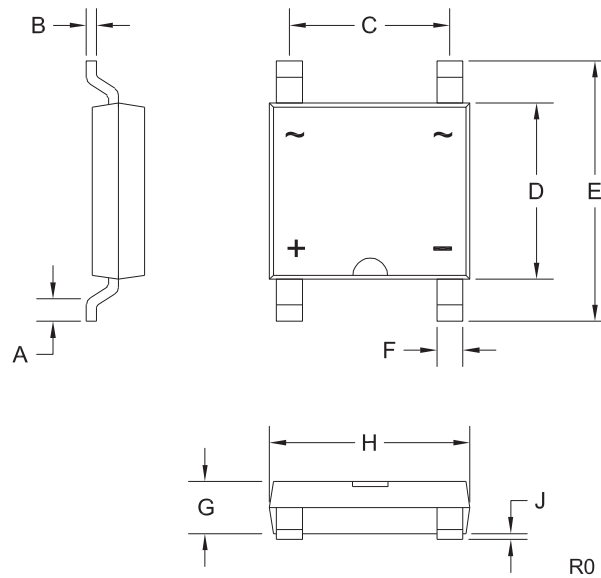
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
$I_R$	$V_R=100V$		0.25	4.0	$\mu A$
$BV_R$	$I_R=150\mu A$	100			V
$V_F$	$I_F=2.0A$		770	840	mV
$C_J$	$V_R=4.0V, f=1.0MHz$		110		pF

Notes: (1) FR-4 epoxy PCB with minimum copper pad area.

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LPDIP CASE - MECHANICAL OUTLINE



DIMENSIONS				
SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.019	0.028	0.50	0.70
B	0.007	0.012	0.20	0.30
C	0.153	0.162	3.90	4.10
D	0.167	0.179	4.25	4.55
E	0.248	0.264	6.30	6.70
F	0.023	0.028	0.60	0.70
G	0.045	0.058	1.15	1.45
H	0.190	0.203	4.85	5.15
J	0.002	0.006	0.05	0.15

LPDIP (REV: R0)

MARKING CODE: CLP2

R3 (17-October 2012)

## OUTSTANDING SUPPORT AND SUPERIOR SERVICES



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### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

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### DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

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### REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

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### CONTACT US

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