SILICON PNP RF TRANSISTOR

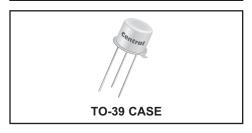


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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CM5160 is a silicon PNP RF transistor, mounted in a hermetically sealed package, designed for high frequency amplifier and non-saturated switching applications. This device is a replacement for the 2N5160.

MARKING: FULL PART NUMBER



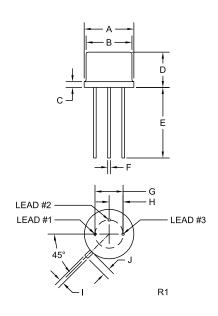
MAXIMUM RATINGS: (TA=25°C unless otherwis	e noted)		
	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	60	V
Collector-Emitter Voltage	V_{CEO}	40	V
Emitter-Base Voltage	V_{EBO}	4.0	V
Continuous Collector Current	IC	0.4	Α
Power Dissipation	P_{D}	1.0	W
Power Dissipation (T _C =25°C)	P_{D}	5.0	W
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +200	°C
Thermal Resistance	$\Theta_{\sf JA}$	175	°C/W
Thermal Resistance	Θ JC	35	°C/W
ELECTRICAL CHARACTERISTICS: (TA=25°C u	ınless otherwise no	oted)	
SYMBOL TEST CONDITIONS	MIN	MAX	UNITS
I _{CBO} V _{CB} =28V		1.0	μΑ

SYMBOL ICBO	TEST CONDITIONS V _{CB} =28V	MIN	MAX 1.0	UNITS μΑ
	V _{CF} =60V		100	μA
I _{CES}	~=		20	•
ICEO	V _{CE} =28V	40	20	μA
BVCEO	I _C =5.0mA	40		V
BVEBO	I _E =100μA	4.0		V
hFE	V _{CE} =5.0V, I _C =50mA	10		
fT	V _{CE} =15V, I _C =50mA, f=200MHz	500		MHz
C_{cb}	V_{CB} =28V, I_E =0, f=0.1 to 1.0MHz		4.0	pF

SILICON PNP RF TRANSISTOR



TO-39 CASE - MECHANICAL OUTLINE



DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
A (DIA)	0.335	0.370	8.51	9.40		
B (DIA)	0.315	0.335	8.00	8.51		
С	-	0.040	-	1.02		
D	0.240	0.260	6.10	6.60		
Е	0.500	-	12.70	-		
F (DIA)	0.016	0.021	0.41	0.53		
G (DIA)	0.200		5.08			
Н	0.100		2.54			
	0.028	0.034	0.71	0.86		
J	0.029	0.045	0.74	1.14		

TO-39 (REV: R1)

LEAD CODE:

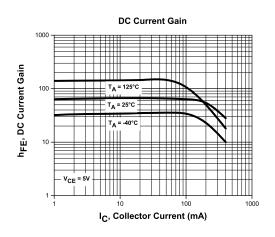
- 1) Emitter 2) Base
- 3) Collector

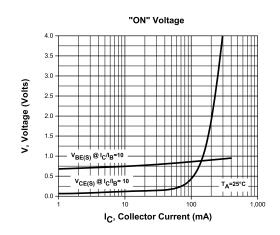
MARKING: FULL PART NUMBER

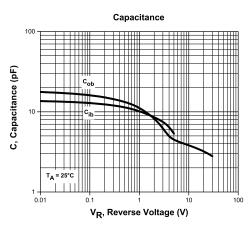
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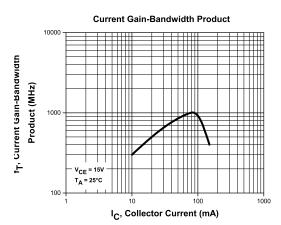


TYPICAL ELECTRICAL CHARACTERISTICS





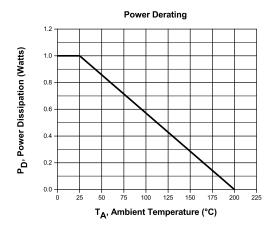


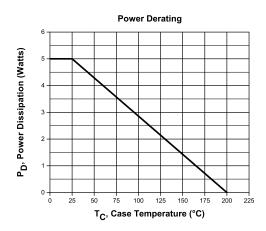


SILICON PNP RF TRANSISTOR



TYPICAL ELECTRICAL CHARACTERISTICS





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