

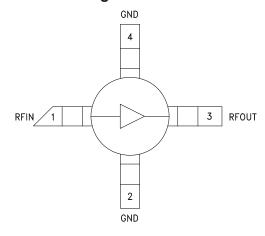
ROHS Designer's Kil

Typical Applications

The HMC474MP86 & HMC474MP86E is an ideal RF/IF gain block for:

- Cellular / PCS / 3G
- Fixed Wireless & WLAN
- CATV, Cable Modem & DBS
- Microwave Radio & Test Equipment

Functional Diagram



HMC474MP86 / 474MP86E

SiGe HBT GAIN BLOCK MMIC AMPLIFIER, DC - 6 GHz

Features

Gain: 15.5 dB P1dB Output Power: +8 dBm Output IP3: +22 dBm Cascadable 50 Ohm I/Os Single Supply: +3V to +10V Included in the HMC-DK001 Designer's Kit

General Description

The HMC474MP86 & HMC474MP86E are general purpose SiGe Heterojunction Bipolar Transistor (HBT) Gain Block MMIC SMT amplifiers covering DC to 6 GHz. This Micro-P packaged amplifier can be used as a cascadable 50 Ohm RF/IF gain stage with up to +10 dBm output power. The HMC474MP86(E) offer 15.5 dB of gain with a +22 dBm output IP3 at 850 MHz while requiring only 25 mA from a single positive supply. The Darlington feedback pair used results in reduced sensitivity to normal process variations and excellent gain stability over temperature while requiring a minimal number of external bias components.

Electrical Specifications, Vs= 5V, Rbias= 110 Ohm, T_A = +25° C

v01.0906

Parameter		Min.	Тур.	Max.	Units
Gain	DC - 1.0 GHz 1.0 - 2.0 GHz 2.0 - 3.0 GHz 3.0 - 4.0 GHz 4.0 - 5.0 GHz	13 12 10 9 8	15.5 14 12 11 10		dB dB dB dB dB
Gain Variation Over Temperature	5.0 - 6.0 GHz DC - 6 GHz	7	9	0.015	dB dB/ °C
Input Return Loss	DC - 1.0 GHz 1.0 - 4.0 GHz 4.0 - 5.0 GHz 5.0 - 6.0 GHz		15 16 19 16		dB dB dB dB dB
Output Return Loss	DC - 5 GHz 5.0 - 6.0 GHz		17 13		dB dB
Reverse Isolation	DC - 4 GHz		17		dB
Output Power for 1 dB Compression (P1dB)	0.5 - 4.0 GHz 4.0 - 5.0 GHz 5.0 - 6.0 GHz	5 4 3	8 7 6		dBm dBm dBm
Output Third Order Intercept (IP3) (Pout= 0 dBm per tone, 1 MHz spacing)	0.5 - 4.0 GHz 4.0 - 5.0 GHz 5.0 - 6.0 GHz		22 20 17		dBm dBm dBm
Noise Figure	DC - 5 GHz 5.0 - 6.0 GHz		3 3.4		dB dB
Supply Current (Icq)			25		mA

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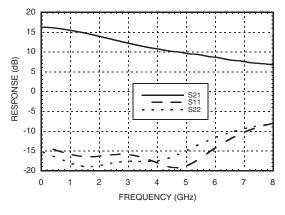
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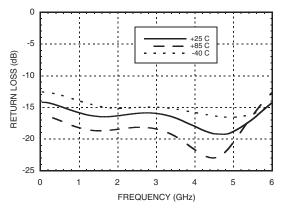




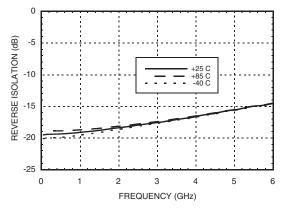
Broadband Gain & Return Loss



Input Return Loss vs. Temperature



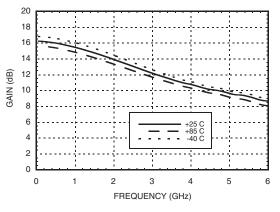
Reverse Isolation vs. Temperature



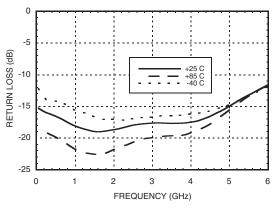
HMC474MP86 / 474MP86E

SiGe HBT GAIN BLOCK MMIC AMPLIFIER, DC - 6 GHz

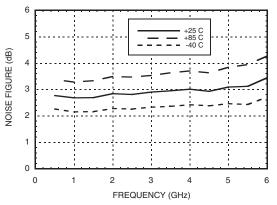
Gain vs. Temperature



Output Return Loss vs. Temperature



Noise Figure vs. Temperature



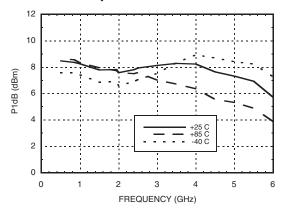
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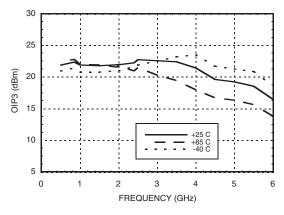




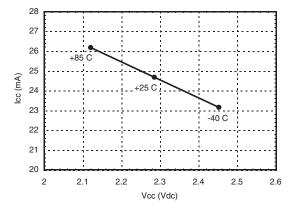
P1dB vs. Temperature



Output IP3 vs. Temperature



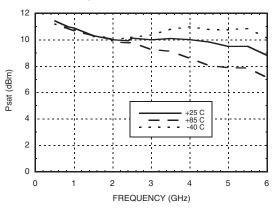
Icc vs. Vcc Over Temperature for Fixed Vs= 5V, R_{BIAS}= 110 Ohms



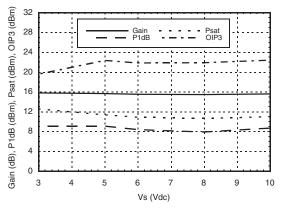
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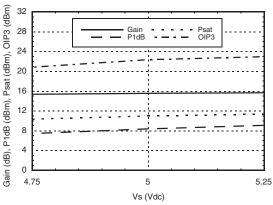
Psat vs. Temperature



Gain, Power & OIP3 vs. Supply Voltage for Constant Icc= 25 mA @ 850 MHz



Gain, Power & OIP3 vs. Supply Voltage for Rs = 110 Ohms @ 850 MHz



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Absolute Maximum Ratings

	•
Collector Bias Voltage (Vcc)	+6.0 Vdc
Collector Bias Current (Icc)	35 mA
RF Input Power (RFIN)(Vcc = +2.4 Vdc)	+5 dBm
Junction Temperature	150 °C
Continuous Pdiss (T = 85 °C) (derate 4.3 mW/°C above 85 °C)	0.280 W
Thermal Resistance (junction to lead)	232 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
ESD Sensitivity (HBM)	Class 1B



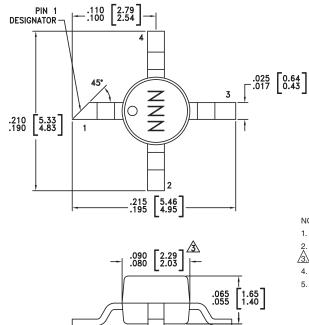
ELECTROSTATIC SENSITIVE DEVICE **OBSERVE HANDLING PRECAUTIONS**

SiGe HBT GAIN BLOCK

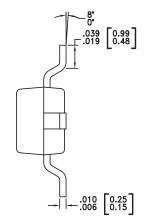
HMC474MP86 / 474MP86E

MMIC AMPLIFIER, DC - 6 GHz

Outline Drawing



.007 0.18



NOTES:

- 1. LEADFRAME MATERIAL: COPPER ALLOY
- 2. DIMENSIONS ARE IN INCHES [MILLIMETERS]
- A DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.
- 4. ALL GROUND LEADS MUST BE SOLDERED TO PCB RF GROUND.

5. THE MICRO-P PACKAGE IS DIMENSIONALLY COMPATIBLE WITH THE "MICRO-X PACKAGE"

Package Information

.047 1.19 .027 0.69

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking	
HMC474MP86	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 ^[1]	474	
HMC474MP86E	C474MP86E RoHS-compliant Low Stress Injection Molded Plastic		MSL1 ^[2]	<u>474</u>	

[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

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HMC474MP86 / 474MP86E

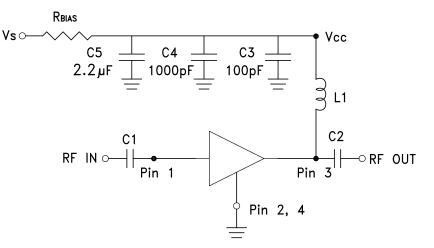
SiGe HBT GAIN BLOCK MMIC AMPLIFIER, DC - 6 GHz



Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1	RFIN	This pin is DC coupled. An off chip DC blocking capacitor is required.	RFOUT
3	RFOUT	RF output and DC Bias (Vcc) for the output stage.	
2, 4	GND	These pins must be connected to RF/DC ground.	

Application Circuit



Recommended Bias Resistor Values for Icc= 25 mA, Rbias= (Vs - Vcc) / Icc

Supply Voltage (Vs)	ЗV	5V	6V	8V	10V
RBIAS VALUE	30 Ω	110 Ω	150 Ω	240 Ω	300 Ω
RBIAS POWER RATING	1/8 W	1/8 W	1/4 W	1/2 W	1/2 W

Note:

- 1. External blocking capacitors are required on RFIN and RFOUT.
- 2. RBIAS provides DC bias stability over temperature.

Recommended Component Values for Key Application Frequencies

Component	Frequency (MHz)							
Component	50	900	1900	2200	2400	3500	5200	5500
L1	270 nH	56 nH	18 nH	18 nH	15 nH	8.2 nH	6.8 nH	3.3 nH
C1, C2	0.01 µF	100 pF						

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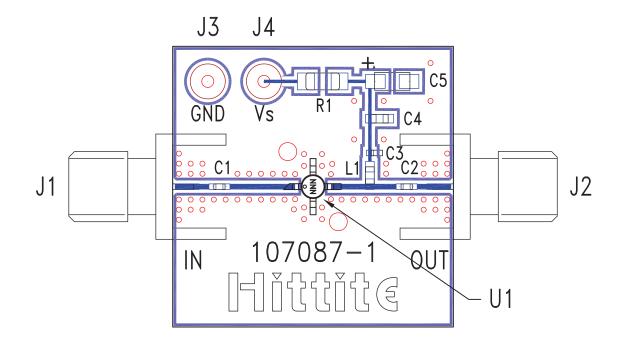


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SiGe HBT GAIN BLOCK MMIC AMPLIFIER, DC - 6 GHz



Evaluation PCB



v01.0906

List of Materials for Evaluation PCB 107179 [1]

Item	Description	
J1 - J2	PCB Mount SMA Connector	
J3 - J4	DC Pin	
C1, C2	Capacitor, 0402 Pkg.	
С3	100 pF Capacitor, 0402 Pkg.	
C4	1000 pF Capacitor, 0603 Pkg.	
C5	2.2 µF Capacitor, Tantalum	
R1	Resistor, 1210 Pkg.	
L1	Inductor, 0603 Pkg.	
U1	HMC474MP86 / HMC474MP86E	
PCB [2]	107087 Evaluation PCB	

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation board should be mounted to an appropriate heat sink. The evaluation circuit board shown is available from Hittite upon request.

9 DRIVER & GAIN BLOCK AMPLIFIERS - SMT

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